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SMOS DPGS Maintenance & Evolution

XML Schemas Release Note

Version 07-04-03

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1. INTRODUCTION

The aim of this document is to provide the configuration status of the XML Schemas software v07-04-03. These schemas can evolve independently of the XML library and for this reason it contains its own configuration identification and can be released alone.

The Schemas Release Note contains the following information:

- Applicable documentation
- Description of version contents
- Description of the means and tools necessary to develop, modify, generate and run the application
- Installation instructions
- List of changes

XML Schemas v07-04-03 shall be installed with XML R/W API SW library v04.00.00 or higher versions.

1.1 ACRONYMS AND TERMS

Refer to document SO-TN-IDR-GS-0010 [AD.2.]

1.2 DOCUMENT OVERVIEW

The Schemas Release Note has been organised as follows:

Section 1, the section you are currently reading

Section 2, applicable and reference documents

Section 3, SMOS Schemas overview

Section 4, content of the delivery

Section 5, installation instructions

Section 6, list of changes

Section 7, XML Schemas list

2. DOCUMENTS

2.1 APPLICABLE DOCUMENTS

Table 2-1: Applicable Documents

Ref.	Title	Code	Version	Date
[AD.1.]	SMOS DPGS Configuration and Data Management Plan	SO-PL-IDR-GS-0003	1.4	28-Mar-06
[AD.2.]	SMOS DPGS Acronyms	SO-TN-IDR-GS-0010	2.1	29-Feb-16
[AD.3.]	XML R/W API Software User Manual	SO-ID-IDR-GS-0009	2.2	09-Jul-10
[AD.4.]	XML Schema Guidelines	SO-MA-IDR-GS-0004	2.1	09-Jul-10
[AD.5.]	Explorer_Data_Handling SUM	CS-MA-DMS-GS-0009	3.5	07-Jul-06
[AD.6.]	PLPC-DPGS ICD	SO-ICD-ESA-GS-1735	2.11	15-Jan-08
[AD.7.]	PDPC-Core Generic IPF ICD	SO-ID-IDR-GS-1001	1.10	26-Nov-07
[AD.8.]	XBAS-L0P ICD	SO-ID-IDR-GS-0017	2.6	08-Jul-10
[AD.9.]	XBAS-CORE ICD	SO-ID-IDR-GS-0018	1.9	24-Apr-09
[AD.10.]	DPGS-Level 1 Processor ICD	SO-ID-IDR-GS-0008	4.17	16-Sep-16
[AD.11.]	DPGS-Level 2 Processor ICD	SO-ID-IDR-GS-0003	6.0	05-Jul-13
[AD.12.]	DPGS-SPGF ICD	SO-ID-IDR-GS-0005	2.3	09-Jul-10
[AD.13.]	DPGS-MF ICD	SO-ID-IDR-GS-0007	1.14	09-Jul-09
[AD.14.]	DPGS-IAT ICD	SO-ID-IDR-GS-0002	2.2	10-Dec-10
[AD.15.]	DPGS-SPQC ICD	SO-ID-IDR-GS-0020	2.4	09-Jul-10
[AD.16.]	L0P-CORE ICD	SO-ID-IDR-GS-0019	2.8	09-Jul-10
[AD.17.]	DPGS-Users ICD	SO-ID-IDR-GS-0010	2.12	09-Jul-10
[AD.18.]	DPGS_FPC-DPGS_RC/LTA ICD	SO-ID-IDR-GS-0014	2.9	09-Jul-10
[AD.19.]	DPGS-MUS ICD	SO-ID-IDR-GS-0015	2.13	09-Jul-10
[AD.20.]	Level 0 Product Specification	SO-TN-IDR-GS-0003	3.5	02-Feb-10
[AD.21.]	Level 1 and Auxiliary Data Product Specification	SO-TN-IDR-GS-0005	6.4	16-Oct-17
[AD.22.]	Level 2 and Auxiliary Data Product Specification	SO-TN-IDR-GS-0006	8.5	Feb-16
[AD.23.]	DPGS Reports Specifications	SO-TN-IDR-GS-0011	2.5	19-Feb-10
[AD.24.]	DPGS Master ICD	SO-ID-IDR-GS-0016	3.10	26-Apr-16
[AD.25.]	NRT Product Format	SO-ID-DMS-GS-0002	4.0	09-Oct-14
[AD.26.]	DPGS-NRTP ICD	SO-ID-DMS-GS-0001	2.5	10-Oct-11

Ref.	Title	Code	Version	Date
[AD.27.]	Implementation in DPGS of IERS Bulletin B	SO-TN-IDR-GS-0041	1.1	22-Oct-10
[AD.28.]	SMOS DPGS to BUFR Converter Interface Control Document	SO-ID-DMS-GS-0004	2.0	10-Feb-11
[AD.29.]	L2 SM Input/Output Data Definition (IODD) Document	SO-ID-ARR-GS-4406	4.3	07-Dec-16
[AD.30.]	SMOS L2 OS Input/Output Data Definition	SO-TN-ARG-GS-0009	2.30	29-Apr-16

3. XML SCHEMAS OVERVIEW

The XML files allow getting information about any data. The XML Schemas are used to define the structure of XML Files. They help to create, modify and validate such XML files in a safer way. The schemas for pure XML files are xml files with .xsd extension and schemas for binary data, constituted by one XML codified with BinX Recommendation, are files with .binXschema.xml extension.

XML Schemas define:

- elements to be considered in a document
- attributes to be considered in a document
- the order of child elements
- the number of child elements
- whether an element is empty or it can include text
- data types for elements and attributes
- default and fixed values for elements and attributes

4. CONTENTS OF THE DELIVERY

The XML Schemas are delivered in a zip file plus a checksum file. The files contain the following information:

- schemas_2018-01-31_v07-04-03.zip.

It contains the following folders, corresponding to the following SMOS file types:

- o AUX_
- o binX
- o CMD_
- o CNF_
- o Common
- o LOG_
- o MIR_
- o MPL_
- o REP_
- o RES_
- o RQC_
- o RQD_
- o TLM_

And the following files:

- o release_2018-01-31_v07-04-03.txt
 - o schemas.cksum
 - o schemas.zip
 - o schemas_config.xml
- schemas_2018-01-31_v07-04-03.cksum, containing the checksum value: 3535394151

The schemas package is located at URL
ftp://131.176.251.166/smos/schemas/schemas_2018-01-31_v07-04-03

5. INSTALLATION INSTRUCTIONS

The XML Schemas Software is delivered in a zip file: schemas_2018-01-31_v07-04-03.zip. Before uncompressing the file, it is recommended to verify that the checksum coincides with the one provided (instruction 'cksum filename').

The schemas tree directory shall be copied into XML R/W API tree directory under smos/schemas folder.

Detailed installation instructions are included in document XML Read/Write API User Manual [AD.3.].

6. CHANGE LIST

Version	Date	Change Description
01-01-00	06-Nov-06	<ul style="list-style-type: none"> • Updated to SO-TN-IDR-GS-0003 L0 Spec v1.8 2006-10-27.doc => DBL_SM_TEST_AUX_LOPGEN_0002.xsd • Added header and zero padding to TLM_MIRA0 => DBL_SM_TEST_TLM_MIRA0__0002.binXschema.xml • Bug in L0 and L1, changed "List_of_Data_Sets" to "List_of_Data_Set": <ul style="list-style-type: none"> * HDR_SM_TEST_AUX_APDL00_0002.xsd * HDR_SM_TEST_AUX_APDS00_0002.xsd * HDR_SM_TEST_AUX_BFP___0002.xsd * HDR_SM_TEST_AUX_DGG___0002.xsd * HDR_SM_TEST_AUX_FAIL___0002.xsd * HDR_SM_TEST_AUX_GANIR__0002.xsd * HDR_SM_TEST_AUX_GLXY___0002.xsd * HDR_SM_TEST_AUX_GLXY___0003.xsd * HDR_SM_TEST_AUX_LOPGEN_0002.xsd * HDR_SM_TEST_AUX_LCF___0002.xsd * HDR_SM_TEST_AUX_MASK___0002.xsd * HDR_SM_TEST_AUX_MOONT__0002.xsd * HDR_SM_TEST_AUX_NIR___0002.xsd



Version	Date	Change Description
		* HDR_SM_TEST_AUX_PATT___0002.xsd
		* HDR_SM_TEST_AUX_PLM___0002.xsd
		* HDR_SM_TEST_AUX_PMS___0002.xsd
		* HDR_SM_TEST_AUX_RFI___0002.xsd
		* HDR_SM_TEST_AUX_SPAR___0002.xsd
		* HDR_SM_TEST_AUX_SUNT___0002.xsd
		* HDR_SM_TEST_AUX_TIME___0002.xsd
		* HDR_SM_TEST_AUX_VTEC_C_0002.xsd
		* HDR_SM_TEST_AUX_VTEC_P_0002.xsd
		* HDR_SM_TEST_MIR_AFWD1A_0002.xsd
		* HDR_SM_TEST_MIR_AFWU1A_0002.xsd
		* HDR_SM_TEST_MIR_BWLD1C_0002.xsd
		* HDR_SM_TEST_MIR_BWLF1C_0002.xsd
		* HDR_SM_TEST_MIR_BWSD1C_0002.xsd
		* HDR_SM_TEST_MIR_BWSF1C_0002.xsd
		* HDR_SM_TEST_MIR_CRSD1A_0002.xsd
		* HDR_SM_TEST_MIR_CRSU1A_0002.xsd
		* HDR_SM_TEST_MIR_FTTD___0002.xsd
		* HDR_SM_TEST_MIR_FTTF___0002.xsd
		* HDR_SM_TEST_MIR_FWSD1A_0002.xsd



Version	Date	Change Description
		* HDR_SM_TEST_MIR_FWSU1A_0002.xsd
		* HDR_SM_TEST_MIR_GMATD__0002.xsd
		* HDR_SM_TEST_MIR_GMATU__0002.xsd
		* HDR_SM_TEST_MIR_JMATD__0002.xsd
		* HDR_SM_TEST_MIR_JMATU__0002.xsd
		* HDR_SM_TEST_MIR_SCLD1C_0002.xsd
		* HDR_SM_TEST_MIR_SCLF1C_0002.xsd
		* HDR_SM_TEST_MIR_SCSD1C_0002.xsd
		* HDR_SM_TEST_MIR_SCSF1C_0002.xsd
		* HDR_SM_TEST_MIR_SC_D1A_0002.xsd
		* HDR_SM_TEST_MIR_SC_D1B_0002.xsd
		* HDR_SM_TEST_MIR_SC_F1A_0002.xsd
		* HDR_SM_TEST_MIR_SC_F1B_0002.xsd
		* HDR_SM_TEST_MIR_TARD1A_0002.xsd
		* HDR_SM_TEST_MIR_TARD1B_0002.xsd
		* HDR_SM_TEST_MIR_TARF1A_0002.xsd
		* HDR_SM_TEST_MIR_TARF1B_0002.xsd
		* HDR_SM_TEST_MIR_UAVD1A_0002.xsd
		* HDR_SM_TEST_MIR_UAVU1A_0002.xsd
		* HDR_SM_TEST_MIR_UNCD1A_0002.xsd



Version	Date	Change Description
		<ul style="list-style-type: none">* HDR_SM_TEST_MIR_UNCU1A_0002.xsd* HDR_SM_TEST_TLM_MIRA1A_0002.xsd
01-02-00	16-Nov-06	<ul style="list-style-type: none">• Updated datablock schema CNF_L0PGEN with SO-ID-IDR-GS-0019 L0P-CORE ICD v1.8 2006-11-14 specifications• Updated datablock schema REP_ACQREP with SO-ID-IDR-GS-0017 XBAS-L0P ICD v1.8 2006-11-14 specifications• Updated L0 schemas with SO-TN-IDR-GS-0003 L0 Spec v1.9 2006-11-14.doc specifications
01-03-00	17-Nov-06	<ul style="list-style-type: none">• Added <Variable_Header> in header AUX_ORBPRES• Minor change CNF_L0PGEN in Type, before CALIBRATION NON-CALIBRATION and now Calibration Non-calibration• Minor change L0_SPH_Main_Info_0003.xsd, added “e” to the field “Precise_Validity_Stop”, therefore minor change in all L0P• Updated datablock schema CNF_L0PGEN with SO-ID-IDR-GS-0019 L0P-CORE ICD v1.8 2006-11-14 specifications• Updated datablock schema REP_ACQREP with SO-ID-IDR-GS-0017 XBAS-L0P ICD v1.8 2006-11-14 specifications• Updated L0 schemas with SO-TN-IDR-GS-0003 L0 Spec v1.9 2006-11-14.doc specifications
01-04-00	17-Nov-06	<ul style="list-style-type: none">• Schemas for level one (L1) and level two (L2) partially finished• Updated schema REP_ACQREP with document SO-ID-IDR-GS-0017 XBAS-L0P ICD v2.0 2006-11-20.doc• Updated schemas CNF_L0PGEN, CNF_L0P1__, CNF_L0P2__, CNF_RPL0P1, CNF_RPL0P2, MPL_ORBSCT and AUX_ORBPRES with document SO-ID-IDR-GS-0019 L0P-CORE ICD v2.0 2006-11-20.doc



Version	Date	Change Description
		<ul style="list-style-type: none">Updated header and datablock schemas L0 with document SO-TN-IDR-GS-0003 L0 Spec v2.0 2006-11-20.doc
01-05-00	17-Nov-06	<ul style="list-style-type: none">Updated header and datablock schemas L1 with document SO-TN-IDR-GS-0005 L1 Spec v3.0 2006-11-24.docAdded Unit="bytes" in L0_SPH_Location_Info_0004.xsd in fields Header_Size and Datablock_SizeMinor change in L0_SPH_Product_Confidence_Data_0004.xsd, type="percentage_10Exp-2_integer_06d_Unit_Type" instead of type="percentage_10_integer_06d_Unit_Type"Schemas for level one (L1) and level two (L2) partially finishedUpdated schema REP_ACQREP with document SO-ID-IDR-GS-0017 XBAS-L0P ICD v2.0 2006-11-20.docUpdated schemas CNF_L0PGEN, CNF_L0P1__, CNF_L0P2__, CNF_RPL0P1, CNF_RPL0P2, MPL_ORBSCT and AUX_ORBPRES with document SO-ID-IDR-GS-0019 L0P-CORE ICD v2.0 2006-11-20.docUpdated header and datablock schemas L0 with document SO-TN-IDR-GS-0003 L0 Spec v2.0 2006-11-20.doc
01-05-01	05-Dec-06	<ul style="list-style-type: none">Added attribute "id" in field FEP of the DBL_SM_TEST_L0PGEN_0005.xsdChanged the type of Grid_Point_Mask from byte to unsignedByte in all L1C datablock schemas:<ul style="list-style-type: none">DBL_SM_TEST_MIR_BWLD1C_0002.binXschema.xmlDBL_SM_TEST_MIR_BWLF1C_0002.binXschema.xmlDBL_SM_TEST_MIR_BWSD1C_0002.binXschema.xmlDBL_SM_TEST_MIR_BWSF1C_0002.binXschema.xmlDBL_SM_TEST_MIR_SCLD1C_0003.binXschema.xml

Version	Date	Change Description
		DBL_SM_TEST_MIR_SCLF1C_0003.binXschema.xml DBL_SM_TEST_MIR_SCSD1C_0003.binXschema.xml DBL_SM_TEST_MIR_SCSF1C_0003.binXschema.xml <ul style="list-style-type: none"> Set the default value to tag File_Name in SM_TEST_MPL_JOBORD_0003.xsd schema. Updated schemas_config.xml with the new version of the schemas.
02-00-00	22-Dec-06	<ul style="list-style-type: none"> Updated header and datablock schemas L0 with document SO-TN-IDR-GS-0003 L0 Spec v2.0 2006-11-24.doc Updated header and datablock schemas L1 with document SO-TN-IDR-GS-0005 L1 Spec v3.0 24-11-2006.doc Updated header and datablock schemas L2 with document SO-TN-IDR-GS-0006 L2 Spec v2.0 2006-11-24.doc Updated all schemas name with XXXX replacing file class. Updated all schemas identifiers name in schemas_config.xml by XXXX.
02-00-01	03-Jan-07	<ul style="list-style-type: none"> Created schemas for CNF_L1OP__ with document SO-ID-IDR-GS-0008 L1P ICD v2 2 2006-10-23.pdf Updated schemas_config.xml with CNF_L1OP__ schemas.
02-00-02	17-Jan-07	<ul style="list-style-type: none"> Updated type of Datablock_Schema tag from 42 characteres to 31 characteres in headers of AUX_APDL__, AUX_APDS__, AUX_BFP__, AUX_CNFL1P, AUX_FAIL__, AUX_LCF__, AUX_NIR__, AUX_PLM__, AUX_PMS__, AUX_SPAR__, AUX_TIME__. In AUX_CNFL1P: Solved problems with NOISE_SOURCE_NUMBER. in AUX_LCF__: The type of LICEF_ID tag has been changed from [ABC][ABC][HV_][0-9]{2} to [ABC][ABC][HV_][0-9]{2}. In AUX_PLM__: Delete fixed values in Intermedaite_Frequency, Low_Frequency, Time_Delay_Minus_T and Time_Delay_Plus_T tags.

Version	Date	Change Description
		<ul style="list-style-type: none"> ○ All types with double_s08lf_x have been changed to double_s05p6lf. And types with double_s018lf have been changed to double_s03p14lf or double_s05p12lf depending on the tag. • In AUX_PMS___: Updated mV_double_s04p5lf_Unit_Type with mV_double_s05p4lf_Unit_Type for Offset tag. • In AUX_TIME__: Updated days_double_s03p8lf_Unit_Type with days_double_s05p6lf_Unit_Type for UTC, UT1, TAI and GPS tags. • In REP_L0_REP: Updated the type of Product_Confidence tag in datablock from enumerated {NOMINAL, ERROR} to {NOMINAL, DEGRADED} • In AUX_DFFFRA: Cumulated_N_Lon;½ precision corrected from float-32 to unsignedInteger-32. • In AUX_DFFXYZ: Cumulated_N_Lon;½ precision corrected from float-32 to unsignedInteger-32. • In AUX_DFFLAI: Cumulated_N_Lon;½ precision corrected from float-32 to unsignedInteger-32. • In AUX_DFFLMX: Cumulated_N_Lon;½ precision corrected from float-32 to unsignedInteger-32. • In AUX_BIGWBF: Big_Water_Body_Flag_Coordinates;½ Dataset defined as fixed array with dimension 0 into the ;½Data_Block_Type;½structure. • In AUX_GAL_SM: TB_Sky_Hi;½ and ;½TB_Sky_Vi;½ expressed as independent matrices instead of two matrices with a common row counter. • In AUX_GALNIR: Arrays structured differently to speed up the reading. • In AUX_GALAXY: Arrays structured differently to speed up the reading. • Updated schemas_config.xml with new version of schemas.
02-00-03	02-Feb-07	<ul style="list-style-type: none"> • Fixed bug in AUX_GALAXY and AUX_GALNIR • In AUX_LCF___: The type of LICEF_ID tag has been changed from [ABC][ABC][HV_][0-9]{2} to [ABC][ABC_][HV_][0-9]{2}.



Version	Date	Change Description
		<ul style="list-style-type: none">In MIR_UAVD1A and UAVU1A: The type of Number of Averaged Measurements tag has been changed to short_03hd_Type.In AUX_APDL00 and APDS00: The type of W tag has been changed from %010.5f to %+012.7f.All schemas for Level 0, level 1 and level 2 products that include Basic_Types schema have been unified with the same version of this schema.
02-00-04	07-Feb-07	<ul style="list-style-type: none">In AUX_LCF___, AUX_FAIL___, AUX_PLM___: The type of LICEF_ID tag has been changed from [ABC][ABC_][HV_][0-9]{2} to (A_ AB B_ BC C_ CA)[HV_](01 02 03 04 05 06 07 08 09 10 11 12 13 14 15 16 17 18 19 20 21).Changed the type of string_date_23_Type and string_date_30_Type in Basic_Types.All schemas for Level 0, level 1 and level 2 products that include Basic_Types schema have been unified with the same version of this schema.
02-00-05	26-Feb-07	<ul style="list-style-type: none">In MPL_ORBSCT: Changed type integer_u_Type by unsigned_Short_hu_TypeIn AUX_PLM___: Changed type double_s05p6lf by double_s07p6lf.Schemas with regular expression [\\-\\+] changed by [-+]:<ul style="list-style-type: none">HDR of AUX_ORBRESHDR of AUX_SPAR___Basic_Types_8HDR and DBL of MPL_SWTREFNew version of CNF_SUBSCR.New version of REP_QC_ACT.
02-00-06	07-May-07	<ul style="list-style-type: none">Bug in TLM_MIRA0 PVT.



Version	Date	Change Description
		<ul style="list-style-type: none">▪ Added L2_REP schemas based on SO-ID-IDR-GS-0003 L2P ICD v1.6 2007-03-20.▪ Updated L1_REP schemas based on SO-ID-IDR-GS-0008 L1P ICD v2.3 2007-02-22.▪ Updated MPL_JOBORDER schema based on SO-ID-IDR-GS-1001_1_8_Generic IPF ICD.▪ Updated AUX_ORBPRES, AUX_ORBRES and REP_TMPRIN schemas, based on SO-ICD-ESA-GS-1735_PLPC-DPGS_ICD_i2r8.▪ Updated MPL_HLPLAN, MPL_XBDOWN and MPL_ORBSCT schemas, based on SO-ID-IDR-GS-0005 DPGS-SPGF ICD V1.7 2007-04-10.▪ In TLM_MIRA0_: changes in order of tags.▪ In DBL_SM_XXXX_TLM_MIRA1A_0003.binXschema.xml: changes in dimension of arrays.▪ In L1_SPH_Specific_Product_Location_C_0007.xsd: tag "Mid_Long" changed by "Mid_Lon".▪ In DBL_SM_XXXX_MIR_JMATX__0003.binXschema.xml: changes in dimension of arrays.
02-00-07	10-May-07	<ul style="list-style-type: none">▪ In HDR_SM_XXXX_MIR_SCLD1C_0007.xsd, HDR_SM_XXXX_MIR_SCLF1C_0007.xsd, HDR_SM_XXXX_MIR_SCSD1C_0007.xsd and HDR_SM_XXXX_MIR_SCSF1C_0007.xsd: changes in L1_SPH_Specific_Product_Location_C_0007.xsd▪ Changed REP_ACQREP by REP_ACQREX.▪ For all REP_header schemas new version of with Fixed_Header_0007 and Basic_Types_0008, except for QC_ANA until new version of SPQC.▪ For all REP_replace TEST by XXXX in schema name, except for QC_ANA until new version of SPQC.▪ Updated REP_XAR___, REP_XIP___ and REP_XDAR___ following SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.5 2006-10-27.doc, with Fixed_Header_0007 and Basic_Types_0008.
02-00-08	14-May-07	New version of L1 and L0 MIR_ schemas because of changes in type of "Leap_Second" tag in Main Product Header. The type of "Leap_Second" has changed from string_date_30_Type to string_date_30_with_blank_Type. String_date_30_with_blank_Type type allows 30 blank value.



Version	Date	Change Description
02-00-09	24-May-07	<ul style="list-style-type: none">• In header of AUX_, MIR_, CNF_ (except TSKTBL), REP_ (except RECEPT and TMPRIN) and TLM added to File_Class_Type RPRO and GSOV.• Replace TEST by XXXX in schemas name, except for QC_ANA until new version of SPQC.• In AUX_ORBPRES and AUX_ORBRES updated schemas. File_Version updated with 3 digits. Changed complex double and long types by a simple type.• Updated REP_XIP___, REP_XAR___ and REP_XDAR___ schemas according to SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.6 2007-05-17.doc.• Updated REP_RECEPT schemas according to SO-ID-IDR-GS-0014 DPGS_FPC-DPGS_RC_LTA ICD v1.6 2007-01-19.doc.• Updated REP_L1_REP and CNF_L1OP___ schemas according to SO-ID-IDR-GS-0008 L1P ICD v2.4 2007-05-08.doc.• Updated REP_L2_REP schemas according to SO-ID-IDR-GS-0003 L2P ICD v1.7 2007-05-08.doc.• In AUX_ORBPRES, AUX_ORBRES, MPL_HLPLAN, MPL_XBDOWN, MPL_ORBSCT, REP_TMPRIN and REP_RECEPT changed use of attribute "type="xml"" by optional in "Data_Block" tag.
02-00-10	29-May-07	<ul style="list-style-type: none">• Changed File_Class_Type in Basic_Types from "TEST OPER RPRO GSOV" to "TEST OPER REPR" for almost all headers, according with SO-ID-IDR-GS-0016 DPGS Master ICD v1.14 2007-05-29.doc.• Changed possible values of File_Type tag in all datablocks schemas of REP/L1_REP.• Upadted Leap_Second type adding 30 blanks as possible value for L2 header schemas.
02-01-00	05-Jul-07	<ul style="list-style-type: none">• Added thirteen new types of products MIR_TEST0_, REP_VTEC_C, REP_VTEC_P, REP_ECMWF_, REP_DFFLAI, REP_SMPSP_, CNF_ECMWF_, CNF_LAI___, CNF_L2OS___, CNF_L2SM___, CNF_SMPSP_, AUX_OEFPRE and AUX_OEFRES.• In DBL_SM_XXXX_AUX_SGLINT_0004.binXschema.xml corrected dimension of variable arrays.

Version	Date	Change Description
		<ul style="list-style-type: none"> In DBL_SM_XXXX_AUX_FOAM___0004.binXschema.xml corrected "Data_Set_Foam_Fraction_Type" with ACRI schema. In DBL_SM_XXXX_AUX_AGDPT__0002.binXschema.xml corrected some tags name. In DBL_SM_XXXX_REP_XAR___0004.xsd and DBL_SM_XXXX_REP_XDAR___0003.xsd chaged Das1_lock and Das2_lock tag names by DAS1_Lock and DAS2_Lock respectively. Updated L2_REP schemas following document SO-ID-IDR-GS-0003 L2P ICD v1.5 2007-02-16.pdf. Updated REP_ACQREX with several changes in the structure. In DBL_SM_XXXX_REP_PRO_DA_0005.xsd, DBL_SM_XXXX_REP_PRO_MO_0003.xsd and DBL_SM_XXXX_REP_PRO_WE_0003.xsd added two new tags for lists of processed and generated tests files. In DBL_SM_XXXX_AUX_CNFSM2_0007.xsd, Guess_prior type corrected from dl_integer_02d_Unit_Type to dl_string_boolean_Unit_Type In L0_SPH_Main_Info_0012.xsd, L1_SPH_Main_Info_0009.xsd and L2_SPH_Main_Info_0009.xsd changed deg_10Exp-6_double_s04p6lf_Unit_Type by deg_double_degs04p6lf_Unit_Type in Long_at_ANX tag. In L2_SPH_Specific_Product_Location_Moisture_0007.xsd added Westernmost_Longitude and Westernmost_Gridpoint_ID tags. In L2_SPH_Specific_Product_Location_Moisture_0007.xsd and L2_SPH_Specific_Product_Location_Salinity_0007.xsd changed deg_10Exp-6_double_s04p6lf_Unit_Type by deg_double_degs04p6lf_Unit_Type in Southernmost_Latitude, Northernmost_Latitude, Easternmost_Longitude and Westernmost_Longitude tag. In DBL_SM_XXXX_AUX_SOIL_P_0002.binXschema.xml, Soil_Porproperties_Coordinates changed from structure to fixed array. In DBL_SM_XXXX_AUX_DISTAN_0003.binXschema.xml, array fixed dimension number corrected from

Version	Date	Change Description
		<p>2621442 to 2621441.</p> <ul style="list-style-type: none"> • Removed Blank spaces included in tags within DBL_SM_XXXX_AUX_ECMWF__0004.binXschema.xml • In DBL_SM_XXXX_AUX_RGHNS2_0004.binXschema.xml dT_h_2 tag renamed as dT_V_2 • In DBL_SM_XXXX_AUX_SSS___0003.binXschema.xml, array fixed dimension number corrected from 2621442 to 2621441. • In DBL_SM_XXXX_REP_SMUDP2_0004.xsd, DBL_SM_REP_SMDAP2_0004.xsd, DBL_SM_XXXX_REP_OSUDP2_0004.xsd, DBL_SM_REP OSDAP2_0004.xsd specified: <ul style="list-style-type: none"> ○ Values for System_Reaction tag: Processing Continued Processing Breakdown Cancelled by System ○ Values for File_Type tag: Constants Configuration Auxiliary Calibration Measurements ○ Values for Product_Result tag: SUCCESSFUL CORRUPTED FAILURE CANCELLED ○ Values for PCD tag: NOMINAL DEGRADED. ○ File_Name tag has been renamed as Filename. • In DBL_SM_XXXX_MIR_OSUDP2_0004.binXschema.xml, blank spaces contained in “Control_Flags” tag have been removed. • In DBL_SM_XXXX_AUX_CNFSM2_0006.xsd, TH_W2_N, TH_W1_N, TH_TS_N, TH_TM_N, TH_S2W_N, TH_S2M_N, TH_S1W_N, TH_S1M_N, TH_R2_N, TH_R1_N, TH_F2_N, TH_NO_N, TH_WL_N, TH_EB_N, TH_EI_N, TH_EU_N, Forward_Model types corrected from integer_03d_Type to string_3_Type • In DBL_SM_XXXX_MIR_SMDAP2_0005.binXschema.xml and DBL_SM_XXXX_MIR_SMUDP2_0005.binXschema.xml, all the fields specified as character-8 changed to unsignedByte-8. The blank spaces have been removed. • In DBL_SM_XXXX_AUX_BIGWBF_0005.binXschema.xml, FL_Big_Water type corrected from byte-8 to unsignedByte-8;

Version	Date	Change Description
		<ul style="list-style-type: none"> In DBL_SM_XXXX_AUX_LANDCL_0008.xsd, Ecosystem_Code and Num_Classes types corrected from integer_03d_Type to unsignedInteger_03u_Type. And TT_H type corrected from double_01p8lf_Type double_02p7lf_Type. In HDR_SM_XXXX_AUX_SOIL_P_0002.xsd, Scaling_Factor_FO field renamed as Scaling_Factor_FC. In SM_TEST_MPL_JOBORD_0004.xsd, File_Name type renamed as Filename_Type, File_Counter has been removed from the Brk_File;½ descendants. File_Counter has been added as Output descendant. In HDR_SM_XXXX_REP_SMDAP2_0004.xsd, HDR_SM_XXXX_REP_SMUDP2_0004.xsd, HDR_SM_XXXX_REP_OSUDP2_0004.xsd, HDR_SM_XXXX_REP OSDAP2_0004.xsd Ref_Doc precision corrected from string to string_17_Type. In L2_SPH_Specific_Product_Location_Moisture_0007.xsd and L2_SPH_Specific_Product_Location_Moisture_0007.xsd the Grid_Points identifiers have been coded as unsigned_Integer_07u_Type instead of integer_s07d_Type. In all L1_REP datablocks changed “Cancelled System” value by “Cancelled by System” value in string_System_Reaction_Type. In DBL_SM_XXXX_AUX_CNFL1P_0009.xsd changed seconds_unsigned_Long_08ld_Unit_Type by seconds_unsigned_Long_08lu_Unit_Type and integer_08d_Type by unsigned_Long_08lu_Type
02-01-01	16-Aug-07	<ul style="list-style-type: none"> Updated DBL_SM_XXXX_CNF_L1OP___0003.xsd schema to BL_SM_XXXX_CNF_L1OP___0006.xsd with Data_Block/Engineering_Parameters/List_of_Supported_Schemas field. Updated SM_XXXX_MPL_JOBORD_0005 to SM_XXXX_MPL_JOBORD_0007. Removed blank space in “LAI_QC” tag name from datablock file DBL_SM_XXXX_AUX_DFFLAI_0004.xsd. In all datablocks files for L1A reports (AFWD1A, AFWU1A, CRSD1A, CRSU1A, FWSD1A, FWSU1A, GMATD_, GMATU_, JMATD_, JMATU_, MIRA1A, NIR_1A, SC_D1A, SC_F1A, TARD1A, TARF1A, UAVD1A, UAVU1A, UNCD1A and UNCU1A) changed “Number_Downlinked_Packets”, “Number_Missing_Packets” and “Number_Corrupted_Packets” tags types by “integer_05d_Type”.

Version	Date	Change Description
		<ul style="list-style-type: none"> In all headers files for L1 reports changed “Ref_Doc” tag type by “string_17_Type” and default value by “SO-ID-IDR-GS-0008”. Corrected schemas name of AUX_OEFPRE and AUX_OEFRES in schemas_config.xml. For reports REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DIS_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE, REP_OPERAL Quality_Flag values changed from “P W F” to “P W F N”. In REP_XAR and REP_XDAR datablocks changed “DAS1_Lock” by “Das1_Lock” and “DAS2_Lock” by “Das2_Lock”. In datablock of AUX_CNFL1P for the ‘MIN_RF_LEVEL_VALUE’, ‘MAX_RF_LEVEL_VALUE’, ‘THRESHOLD_RF_LEVEL_VALUE’, ‘MIN_VCO_TM_VALUE’, ‘MAX_VCO_TM_VALUE’, ‘THRESHOLD_VCO_TM’, ‘MIN_NUM ALIM_TM_VALUE’, ‘MAX_NUM ALIM_TM_VALUE’, ‘THRESHOLD_NUM ALIM_TM’, ‘MIN_SSPA ALIM_TM_VALUE’, ‘MAX_SSPA ALIM_TM_VALUE’ and ‘THRESHOLD_SSPA ALIM_TM’ elements added ttribute ‘unit=V’.
02-01-02	10-Sep-07	<ul style="list-style-type: none"> Added DBL_SM_XXXX_AUX_AGDPT__0003.binXschema.xml to put inline with ACRI schema. Added DBL_SM_XXXX_AUX_CNFOS2_0008.xsd to put inline with ACRI schema. Added DBL_SM_XXXX_AUX_GAL_OS_0003.binXschema.xml to put inline with ACRI schema. Added DBL_SM_XXXX_AUX_RGHNS1_0004.binXschema.xml to put inline with ACRI schema. Added DBL_SM_XXXX_MIR_SMDAP2_0006.binXschema.xml to correct X_Swath type form unsigned integer 32 to signed integer 32. Removed “L0P_HW_Identifier” element from DBL_SM_XXXX_CNF_L0P1__0005.xsd. Removed “L0P_HW_Identifier” element from DBL_SM_XXXX_CNF_L0P2__0006.xsd. Updated List_of_Supported_Schemas and added “FARADAY” tag in DBL_SM_XXXX_CNF_L10P__0007.xsd.

Version	Date	Change Description
		<ul style="list-style-type: none"> In DBL_SM_XXXX_REP_PRO_DA_0007.xsd: Updated values for “Subtitle” tag: “Products from OPER Orders Products from RPRO Orders”. Increased number of digits for “Sensing_Time” tag until 10 digits. Increased number of digits for “Processing_Duration” tag until 6 digits.
02-02-00	25-Sep-07	<p>This delivery only works from XML R/W API version 2007-09-24_v03-07-00 on.</p> <ul style="list-style-type: none"> Added new category of schemas LOG_. Added new type of schemas “LOG_FLUSH_”. In HDR_SM_XXXX_AUX_ECMWF__0009.xsd changed the type of Overall_Quality adding a new value “3” to the type. In DBL_SM_XXXX_AUX_ECMWF__0005.xsd changed the type of tag “Land_Sea_Mask” by “unsignedByte-8” and the type of tag “Sea_Ice_Cover” by “float-32”. In DBL_SM_XXXX_REP_PRO_DA_0008.xsd removed tags List_of_DailyTESTGeneratedProducts and List_of_DailyTESTPorcessed_Aux_Data. In HDR_SM_XXXX_REP_DFFLAI_0002.xsd, HDR_SM_XXXX_REP_ECMWF__0002.xsd and HDR_SM_XXXX_REP_SMPSP__0002.xsd removed tag Total_Size. In DBL_SM_XXXX_CNF_SMPSP__0002.xsd changed tag Processor_Executable by five tags: TLV_Executable, TFO_Executable, ROU_Executable, RFI_Executable and FLO_Executable.
02-02-01	22-Oct-07	<ul style="list-style-type: none"> SCH-PR-2 (DPGS-PR-369). Updated REP_PRO_DA, REP_PRO_GL, REP_PRO_MO and REP_PRO_WE following the last report specification document. SCH-PR-1 (DPGS-PR-366). Updated length for “File_Name” tag in REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DI_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO and REP_PRO_WE datablock schemas.
02-02-02	22-Dec-07	<ul style="list-style-type: none"> Changed order in the pattern for “File_Name” tag in REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DI_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO and REP_PRO_WE datablock schemas.



Version	Date	Change Description
02-03-00	18-Feb-08	<ul style="list-style-type: none">Added new schemas for REP_LTA___ and CNF_SUBSCR.Updated REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DI_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO and REP_PRO_WE datablock schemas.Updated REP_QC_ACT, REP_QC_ANA, RQC_SE_DES and RQD_SE_DES following new specification.Fixed bugs in REP_XAR___, REP_XDAR___ and REP_XIP___ schemas.
02-03-01	11-Mar-08	<ul style="list-style-type: none">Updated restriction for "File_Name_Type" type in REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DI_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_OPERAL, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE and REP_SUBSCR datablock schemas.New type "string_Vector_Source_With_Blank_Type" for GMAT and JMAT header. The allowed values are: "IG FP FR ".Fixed bug in Thermistor_ID type in the AUX_FAIL___ datablock.
02-04-00	23-May-08	<ul style="list-style-type: none">In datablocks reports schemas: DIS_DA, DIS_GL, DIS_MO and DIS_WE replaced "Package_Id" type by unsigned_Long_010lu_Type.New schemas for reports QC_ACT and QC_ANA.Unified REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, AND REP_CHE_WE product schemas in only one schema model with a new tag "Time_Coverage" to distinguish the period of coverage.Unified REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, AND REP_CLE_WE product schemas in only one schema model with a new tag "Time_Coverage" to distinguish the period of coverage.Unified REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, AND REP_DIS_WE product schemas in only one schema model with a new tag "Time_Coverage" to distinguish the period of coverage.In REP_DIS_XX: added a new enumerated value "A" for "Quality_Flag".



Version	Date	Change Description
		<ul style="list-style-type: none">Unified REP_INV_DA, REP_INV_GL, REP_INV_MO, AND REP_INV_WE product schemas in only one schema model with a new tag "Time_Coverage" to distinguish the period of coverage.In REP_INV_XX: added a new enumerated value "A" for "Quality_Flag".Unified REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, AND REP_PRO_WE product schemas in only one schema model with a new tag "Time_Coverage" to distinguish the period of coverage.In AUX_APDS00 datablock: updated List_of_Apodisation_Coefficients number of elements.
02-04-01	05-Jun-08	<ul style="list-style-type: none">Job Order schema updated. "State tag" default value changed from Alternative to Non Alternative.REP_ECMWF_ Datablock updated.REP_DFFLAI_ Datablock updated.
02-04-02	12-Jun-08	<ul style="list-style-type: none">Added schemas NRT: AUX_APDNRT, AUX_CNFNRT, AUX_NRTMSK, MIR_BWND1C, MIR_BWNF1C, MIR_SCND1A, MIR_SCND1B, MIR_SCND1C, MIR_SCNF1A, MIR_SCNF1B, MIR_SCNF1C, REP_BWND1C, REP_BWNF1C, REP_SCND1C and REP_SCNF1C.
02-04-03	20-Jun-08	<ul style="list-style-type: none">New CNF_LAI___ schemas added in accordance with SO-ID-GMV-GS-4402_SMOS L2 Internal ICD_v1.6New CNF_ECMWF_ schemas added in accordance with SO-ID-GMV-GS-4402_SMOS L2 Internal ICD_v1.6
Schemas for DPGS-V2		
03-00-00	31-Oct-07	<ul style="list-style-type: none">Updated schemas for L0 and L1 products and ADFs following specifications for V2.Note that the directory structure has been changed. For example, L1_REP has been deleted and all the reports files have been allocated in directories under REP_ folder.
03-01-00	09-Nov-07	<ul style="list-style-type: none">Schemas for L0 and L1 products and ADFs following specifications for V2.Schemas for L2 products, ADFs, reports and configuration files for CORE V3.Note that the directory structure has been changed. For example, L1_REP has been deleted and all the

Version	Date	Change Description
		reports files have been allocated in directories under REP_ folder.
03-02-00	19-Nov-07	<ul style="list-style-type: none"> Fixed bug for Xerces version 2.6.0, when “-“ or “.” Appears without “\”. Fixed bug for schemas when “count” attribute with “unsigned_Integer_hd_Type” type. Updated schemas_config.xml structure as the schemas directory tree. Removed from schemas_config.xml references to binx schemas. In SC_X1B changed “Scena_Bias_Correction” by “Scene_Bias_Correction”. In AUX_PATT__ removed Average_Pattern_Low_Amplitude. This schema package is required from the 2007-11-19_v04-00-00 XML_RW_API version.
03-03-00	21-Nov-07	<ul style="list-style-type: none"> In datablocks of MIR_AFWD1A and MIR_AFWU1A updated fixed arrays of “FWF_Measurements”, “A”, “B”, “C”, “D”, “E” and “FWF_Shpaee_Quality” from two dimensional array to one dimensional array. In datablocks of MIR_CRSD1A and MIR_CRSU1A updated fixed arrays of “FWF_Origin”, “FWF_Origin_Quality”, “FWF_Origin_Amplitude_Sensitivity” and “FWF_Origin_Phase_Sensitivity” from two dimensional array to one dimensional array. New specification V2 for RQC_SE_DES, RQD_SE_DES, REP_QC_ACT and REP_QC_ANA
03-03-01	05-Dec-07	<ul style="list-style-type: none"> Fixed bug in MIR_AFWD1A and MIR_AFWU1A: Changed FWF_Shape_Quality array elements from unsignedInteger-32 to unsignedByte-8 New specification V2 for AUX_SGLINT. New report schemas L1 for V2 based on SO-ID-IDR-GS-0008 L1P ICD v3 0 2007-11-12 specification. New CNF_L1OP__ schemas for V2. In AUX_PLM__ datablock the type of “X”, “Y” and “Z” tags has been changed in order to add more precision.



Version	Date	Change Description
03-03-02	20-Dec-07	<ul style="list-style-type: none">Fixed bug in AUX_SGLINT: Added Sun Incidence Angle Data.Added new tag "Orbit_Scenario_File_Path" in DBL_SM_XXXX_CNF_L1OP__0101 datablock.
03-03-03	25-Jan-08	<ul style="list-style-type: none">MIR_ANIR1A: Changed unsignedByte-8 type by character-8 type in "Correlator_Layer" tag.CNF_L1OP__: Changed string_4_Type by integer_04d_Type in "Unc_Averaged_Measurements_Threshold" and "FWF_Averaged_Measurements_Threshold" tags.MIR_CRSD1A and MIR_CRSU1A: Changed unsignedByte-8 type by character-8 type in "Correlator_Layer" node.AUX_PMS__: Changed K_float_02p3f_Unit_Type by K_float_03p3f_Unit_Type in "Temperature" tag.AUX_NIR__: Changed db_float_02p2f_Unit_Type by db_float_s03p2f_Unit_Type in "Amplitude_Dicke_Switch_V" and "Amplitude_Dicke_Switch_H" tags.AUX_BWGHT_: Changed "Baseline_ID" restriction type.Removed "Number_of_Generated_Snapshot", "Number_of_Corrupted_Snapshot_PC" and "Number_of_Corrupted_Snapshot_MH" tags from datablock of the L1B and L1C reports.
03-03-04	31-Jan-08	<ul style="list-style-type: none">Added Quality_Information structure to MIR_FTTD__, MIR_FTTF__, MIR_GMATD_, MIR_GMATU_, MIR_JMATD_, MIR_JMATU_ header schemas.Replaced "Averaged_FFT_Visib_H" and "Averaged_FFT_Visib_V" by "Averaged_FTT_Visib_H" and "Averaged_FTT_Visib_V" in the MIR_FTTD__ datablock schema.
03-03-05	11-Mar-08	<ul style="list-style-type: none">Updated restriction for "File_Name_Type" type in REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DI_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_OPERAL, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE and REP_SUBSCR datablock schemas.New type "string_Vector_Source_With_Blank_Type" for GMAT and JMAT header. The allowed values are:

Version	Date	Change Description
		<p>“IG FP FR “.</p> <ul style="list-style-type: none"> Fixed bug in Thermistor_ID type in the AUX_FAIL__ datablock. Replaced “CORRUPTED” by “SUSPECT” for “string_Product_Result_Type” in REP_DFFLAI, REP_ECMWF_, REP_OSDAP2, REP_OSUDP2, REP_SMDAP2, REP_SMPSP_ and REP_SMUDP2 datablocks.
03-04-00	06-May-08	<ul style="list-style-type: none"> Unified REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, AND REP_CHE_WE product schemas in only one schema model with a new tag “Time_Coverage” to distinguish the period of coverage. Unified REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, AND REP_CLE_WE product schemas in only one schema model with a new tag “Time_Coverage” to distinguish the period of coverage. Unified REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, AND REP_DIS_WE product schemas in only one schema model with a new tag “Time_Coverage” to distinguish the period of coverage. In REP_DIS_XX: added a new enumerated value “A” for “Quality_Flag”. Unified REP_INV_DA, REP_INV_GL, REP_INV_MO, AND REP_INV_WE product schemas in only one schema model with a new tag “Time_Coverage” to distinguish the period of coverage. In REP_INV_XX: added a new enumerated value “A” for “Quality_Flag”. Unified REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, AND REP_PRO_WE product schemas in only one schema model with a new tag “Time_Coverage” to distinguish the period of coverage. In AUX_APDS__ datablock: pdated “List_of_Apodisation_Coefficients” number of elements. In AUX_SPAR__ datablock: updated “Temperature” format type. Added a new type AUX_BSCAT_ to represent L1 Bistatic Scattering Coefficients. Updated AUX_SGLINT datablock to represent L2 Bistatic Scattering Coefficients. In CNF_L2OS__ datablock: added minOccurs and maxOccurs to all List_*_Types.



Version	Date	Change Description
		<ul style="list-style-type: none">Added last version of CNF_L1OP__ to schemas_config.xml.Updated "File_Name" type in REP_DFFLAI, REP_ECMWF, REP_VTEC_C and REP_VTEC_P datablock schemas.Added schemas NRT: AUX_APDNRT, AUX_CNFNRT, AUX_NRTMSK, MIR_BWND1C, MIR_BWNF1C, MIR_SCND1A, MIR_SCND1B, MIR_SCND1C, MIR_SCNF1A, MIR_SCNF1B, MIR_SCNF1C, REP_BWND1C, REP_BWNF1C, REP_SCND1C and REP_SCNF1C.
03-05-00	20-May-08	<ul style="list-style-type: none">New L0 schemas based on specifications: SO-ID-IDR-GS-0019 L0P-CORE ICD v2.3 and SO-TN-IDR-GS-0003 L0 Spec v3.1.In datablocks reports schemas: DIS_DA, DIS_GL, DIS_MO and DIS_WE replaced "Package_Id" type by unsigned_Long_010lu_Type.
03-05-01	23-May-08	<ul style="list-style-type: none">New L0 schemas based on specifications: SO-ID-IDR-GS-0019 L0P-CORE ICD v2.3 and SO-TN-IDR-GS-0003 L0 Spec v3.1.New schemas for reports QC_ACT and QC_ANA.Fixed bug in schemas_config.xml for AUX_BSCAT_.
03-05-02	27-May-08	<ul style="list-style-type: none">New CNF_L0P1 and CNF_L0P2 schemas based on SO-ID-IDR-GS-0019 L0P-CORE ICD v2.3New schema for MPL_JOBORD
03-05-03	11-Jun-08	<ul style="list-style-type: none">MIR_SC_F0_ and MIR_SC_D0_ schemas have been updated. The allowed values for "Correlator_Layer_Info" have been changed according to SO-TN-IDR-GS-0003 L0 Spec v3.1TLM_MIRA0_ schema has been updated. The allowed values for "Correlator_Layer_Info" have been changed according to SO-TN-IDR-GS-0003 L0 Spec v3.1MPL_JOBORD schema has been updated. The default value for "State" tag has been changed from "Alternative" to "Non Alternative".



Version	Date	Change Description
03-05-04	13-Jun-08	<ul style="list-style-type: none">MIR_CORD0_, MIR_CORU0_, MIR_TARD0_, MIR_TARF0_, MIR_UNCD0_, MIR_UNCU0_ and MIR_TEST0_ schemas have been updated. The allowed values for "Correlator_Layer_Info" have been changed according to SO-TN-IDR-GS-0003 L0 Spec v3.1
03-05-05	11-Jul-08	<ul style="list-style-type: none">The format of the following fields (belonging to L0-SPH products) have been changed from %06d to %09d in accordance with SO-TN-IDR-GS-0003 L0 Spec v3.2 2008-06-20.doc: N_Valid_Packets, N_Missing_Packets, N_Missing_24packet_Blocks, N_Invalid_Blocks and N_RS_Corrected_Bytes
03-05-06	18-Sep-08	<ul style="list-style-type: none">AUX_SPAR_ Datablock updated. The number of "Switch_Value" DSRs allowed changed from 10 to 3
03-05-07	15-Oct-08	<ul style="list-style-type: none">The following schemas have been updated according to SO-TN-IDR-GS-0011 REPORTS Specifications v2 2: REP_CHE_DA, REP_CHE_GL, REP_CHE_MO, REP_CHE_WE, REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_CONF_CO, REP_DIS_DA, REP_DIS_GL, REP_DIS_MO, REP_DIS_WE, REP_INV_DA, REP_INV_GL, REP_INV_MO, REP_INV_WE, REP_LOGCOR, REP_OPERAL, REP_PER_DA, REP_PER_GL, REP_PER_MO, REP_PER_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE.The REP_SUBSCR HDR schema has been updated: The "File_Name" restrictions have been removed for this kind of report.The CNF_L1OP__ Data block schema has been updated. "Sequence_Type" changed to allow values from 00 to 99.MPL_XBDOWN Data block schema has been updated according to SO-ICD-ESA-GS-0967 v2.11 04-02-2008MPL_PROTEV Data block schema has been updated due to changes in the ICD PLPC-SOGS
03-05-08	21-Oct-08	<ul style="list-style-type: none">REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_PER_DA, REP_PER_GL, REP_PER_MO, REP_PER_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE DBL schemas have been updated: The "File_Type" type has been changed in order to allow writing the AUX_GAL2OS file type.



Version	Date	Change Description
		<ul style="list-style-type: none">MPL_XBDOWN DBL schema has been updated: "List_Of_Ground_Stations" tag renamed as "List_of_Ground_Stations"New MPL_APIDPL schemas have been added to the distribution.
03-05-09	22-Oct-08	<ul style="list-style-type: none">REP_CLE_DA, REP_CLE_GL, REP_CLE_MO, REP_CLE_WE, REP_PER_DA, REP_PER_GL, REP_PER_MO, REP_PER_WE, REP_PRO_DA, REP_PRO_GL, REP_PRO_MO, REP_PRO_WE DBL schemas have been updated: The "File_Type" type has been changed in order to allow writing any product file type.MPL_PROTEV DBL schema corrected in order to allow values from 1 to 35 in the "Event_Number" field.New AUX_CNFNRT Data Block schema delivered by Deimos, has been included into DPGS-V2 schemas distribution.AUX_APDS__ Datablock schema has been corrected. Number of "Apodisation_Coefficient" changed to 1396
03-05-10	19-Dec-08	<ul style="list-style-type: none">CNF_WSCONF schema has been added.MPL_RULESF schema has been corrected.REP_LTA__ schema has been updated.REP_XAR__ and REP_XDAR__ schemas have been updated according to SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.8 2008-11-03 (DPGS-PR-1156)
Schemas for DPGS-V3		
04-00-00	24-Jul-08	<ul style="list-style-type: none">L1 schemas updated following specifications for DPGS-V3.
04-00-01	01-Aug-08	<ul style="list-style-type: none">MIR_CRSD1A/MIR_CRSU1A Header schemas updated: Counters for Cons_Ampl_FWF_Origin removed and added for Cons_Phase_FWF_OriginAUX_BFP__, AUX_MISP__ and AUX_PMS__ Data block schemas updated according to SO-TN-IDR-GS-0005 V5.3



Version	Date	Change Description
		<ul style="list-style-type: none">MIR_SCSD1C, MIR_SCSF1C, MIR_SCLD1C, MIR_SCLF1C, MIR_BWSD1C, MIR_BWSF1C, MIR_BWLD1C, MIR_BWLF1C Header Schemas updated according to SO-TN-IDR-GS-0005 V5.3: Geometric_Correction_Type field removedAUX_CNFL1P Data block schema updated according to SO-TN-IDR-GS-0005 V5.3: Geometric_Correction_Type field removed
04-00-02	05-Sep-08	<ul style="list-style-type: none">Reports schema versions updated for DPGS-V3The schema associated to REP_VTEC_R has been added.MIR_SCSD1C Datablock schema corrected since line 67 was corrupted and line 68 had a bug. This information was provided by ARGANS (e-mail)AUX_BSCAT Datablock schema updated. Number of DSR corrected from 1 to 76.AUX_CNFL1P Datablock schema updated. New field "Geometric_Correction_Type" added into the "Algorithm_Configuration" structure.MIR_CRSD1A /MIR_CRSU1A Datablock schemas updated. The index-dimensions have been changed from 71*36 to 2556
04-00-03	19-Sep-08	<ul style="list-style-type: none">AUX_NIR___ Datablock corrected. The exponential precision has been changed from 3 digits to 2 digits in the following fields: C_A_V, D_A_V, U_A_V, A_V, B_V, C_R_V, D_R_V, U_R_V, C_A_H, D_A_H, U_A_H, A_H, B_H, C_R_H, D_R_H, U_R_H.AUX_SPAR_ Datablock updated. The number of "Switch_Value" DSRs allowed changed from 10 to 3
04-00-04	17-Oct-08	<ul style="list-style-type: none">MIR_SCLF1C Data block has been updated: The "X-Band" and "Quality_Information" mistakes have been correctedAUX_CNFL1P Data block has been updated: "RMS_Threshold_for_NIR" field has been added.The CNF_L1OP__ Data block schema has been updated. "Sequence_Type" changed to allow values from 00 to 99.

Version	Date	Change Description
04-00-05	24-Oct-08	<ul style="list-style-type: none"> AUX_PLM DBL schema has been corrected: <ul style="list-style-type: none"> → “Time_Delay_Minus_T” and “Time_Delay_Plus_T” formats have been changed from %+012.6f to %+015.6f according to L1PP formats. → “M_Cal_Nom”, “Q_Cal_Nom”, “M_Cal_Red”, “Q_Cal_Red” formats have been changed from %+08.5f to %+09.5f according to L1PP formats. → “R1” field format has been changed from %18.13f to %+18.13f according to L1PP formats. → “PMS_Range” field format has been changed from %d to %02d according to L1PP formats. AUX_PMS DBL schema has been corrected: “Gain_Sensitivity” and “PMS_Linearity” types have been corrected. Two bugs were reported by L1PP Team. AUX_NIR DBL schema has been corrected: “C_A_V”, “D_A_V”, “U_A_V”, “A_V”, “B_V” formats have been changed in accordance with L1PP products.
04-00-06	07-Nov-08	<ul style="list-style-type: none"> AUX_APDS__ Data block schema has been corrected. Number of “Apodisation_Coefficient” changed to 1396 AUX_CNFL1P Data block schema has been corrected: “Sequence_Type” type has changed in order to allow values from 00 to 99. AUX_PMS Data block schema has been updated. The “Gain_Sensitivity” type has been modified in order to allow the needed values. The change has been requested by GMV (See e-mail 30/10/08).
04-01-00	28-Nov-08	<ul style="list-style-type: none"> L2SM Core-V4 (DPGS-V3) schemas delivered by Array have been added to the schemas distribution. AUX_BNDLST, AUX_ECOLAI, AUX_ECMWF_, AUX_DFFLAI, CNF_BNDLST, CNF_ECOLAI, CNF_ECMWF_, CNF_LAI_, CNF_SMPSP, REP_BNDLST and REP_ECOLAI schemas delivered by GMV have been included in the schemas distribution.
04-02-00	19-Nov-08	<ul style="list-style-type: none"> L2OS Core-V4 (DPGS-V3) schemas delivered by ARGANS have been added to the schemas distribution

Version	Date	Change Description
		<ul style="list-style-type: none"> • The bug reported by GMV in the schemas_config for the AUX_ECOLAI schema has been corrected (SCH-PR-0005) • REP_SC_F1B, REP_SC_F1B, REP_TARD1B, REP_TARF1B Data block schemas have been modified according to SO-ID-IDR-GS-0008 L1P ICD v4.2 2008-12-19 • CNF_L1OP__ schema has been updated according to SO-ID-IDR-GS-0008 L1P ICD v4.2 2008-12-19 • -AUX_CNFL1P Data block schema has been changed: <ul style="list-style-type: none"> • “Reconstruction_Image_Algorithm” allowed values changed from 1,2 to 0,1,2. • “Attitude” field removed • AUX_NIR Data block schema has been changed: “Amplitude_Dicke_Switch_V” and “Amplitude_Dicke_Switch_H” formats have been modified from %+06.3f to %+05.2f. • MIR_AFW1A, MIR_AFWU1A Data block schemas have been updated. The “Quality_Structure” has been added. • MIR_SC_D1B, MIR_SC_F1B, MIR_TARD1B, MIR_TARF1B Header schemas have been updated. “Reconstruction_Image_Algorithm” allowed values have been changed from 1,2 to 0,1,2.



Version	Date	Change Description
04-02-01	30-Jan-09	<ul style="list-style-type: none">The schemas associated to the following files have been included in the distribution: CNF_FTTGEN, CNF_FWFCLP, CNF_GMATR_, CNF_JMATR_, CNF_PRL1BD, CNF_PRL1BF, CNF_VTECGN.The DBL_SM_XXXX_CNFSMPSP__0201 schema, delivered by GMV on Jan 16th, has been added to the distribution.The schemas for the REP_QC_ACT and RQC_SE_DES have been modified according to SO-ID-IDR-GS-0020 DPGS-SPQC ICD v2.3 document.New schema has been added to the distribution for the CNF_SUBSCR fileThe restriction applied to the values of the "PCD" tag in the L1-Reports schemas has been removed. The values defined in SO-ID-IDR-GS-0008 L1P ICD v4.2 2009-01-13 document for this field are allowed."Attitude_Correction" tag included in the AUX_CNFL1P data block has been renamed to "Altitude_Correction", according to SO-TN-IDR-GS-0005 L1 Spec v5.5
04-03-00	10-Feb-09	<ul style="list-style-type: none">New schemas delivered by DMS have been added to the distribution for the following NRT products: AUX_APDNRT, AUX_CNFNRT, MIR_BWND1C, MIR_BWNF1C, MIR_SCND1A, MIR_SCND1B, MIR_SCND1C, MIR_SCNF1A, MIR_SCNF1B, MIR_SCNF1C, REP_BWND1C, REP_BWNF1C, REP_SCND1A, REP_SCND1B, REP_SCND1C, REP_SCNF1A, REP_SCNF1B, REP_SCNF1C.The Basic_Types referenced in DBL_SM_XXXX_REP_QC_ACT and HDR_SM_XXXX_REP_QC_ACT schemas has been harmonized to v0016.The following schemas associated to the L2OS products have been changed because they are not compliant with the ADF samples provided by ARGANS:<ul style="list-style-type: none">AUX_CNFOSD and AUX_CNFOSF Data block schemas have been modified in order to allow the list of "Prior_Errors"Corrected a bug found in the AUX_FLTSEA and AUX_RGHNS3 Data block schemas provided by ARGANS.

Version	Date	Change Description
		<ul style="list-style-type: none"> REP_BNDLST, REP_DFFLAI, REP_ECMWF_, REP_OSUDP2 and REP OSDAP2 Data block schemas have been modified: The values allowed for the "Error_Name" and "Warning_Name" fields have been changed according to SO-ID-IDR-GS-0003 v4.0
04-03-01	20-Feb-09	→ The "Basic_Types", included in the following Data block schemas, have been harmonized to the v0016: REP_SC_D1B, REP_SC_F1B, REP_TARD1B and REP_TARF1B.
04-03-02	03-Mar-09	<p>→ The following changes have been made in the NRT schemas:</p> <ol style="list-style-type: none"> AUX_CNFNRT Header and Data block schemas have been updated in order to harmonize the version of the "Basic_Types" file referenced in the "includes". AUX_NRTMSK Header schema has been changed in order to include the "Auxiliary_L1_Main_SPH_for_Binary_0010.xsd" and "Auxiliary_SPH_Data_Sets_0011.xsd" associated to the files with binary Data block instead of the "Auxiliary_L1_Main_SPH_for_XML_0010.xsd" defined for pure XML files. The "/common/common/Fixed_Header" schema has been also updated to use in all the "includes" the same version of the "Basic_Types" file. REP_BWND1C, REP_BWNF1C, REP_SCND1C and REP_SCNF1C Header and Data block schemas have been updated in order to harmonize the version of the "Basic_Types" file. <p>→ The AUX_CNFSMF/AUX_CNFSMD Data block schemas have been modified after SM Core V4 FAT:</p> <ul style="list-style-type: none"> k2_Tau_H20 unit has been changed to neper Tau_Nad_Min/Max unit has been changed to neper TH_SCENE_TAU_FO unit has been changed to neper TH_TEC unit has been changed to 10**16 electrons/m**2 <p>→ The AUX_CNFL1P Data block schema has been modified according to SO-TN-IDR-GS-0005 L1 Spec v5.6:</p> <ul style="list-style-type: none"> Direct_Sun_Correction_External_Type, Direct_Moon_Correction_External_Type, Earth_Contribution_Removed_External, Sky_Contribution_Removed_External, H_Pol_Max_BT_Sea, V_Pol_Max_BT_Sea, H_Pol_Max_BT_Land, V_Pol_Max_BT_Land and HV_Pol_Max_BT_module fields have been added.



Version	Date	Change Description
04-03-03	11-Mar-09	<ul style="list-style-type: none">→ DBL_SM_XXXX_AUX_PLM____0202.xsd delivered by DMS has been added to the DPGS schemas distribution.→ DBL_SM_XXXX_CNF_L2OS____0201.xsd delivered by ARGANS has been added to the DPGS schemas distribution.→ REP_SC_D1B, REP_SC_F1B, REP_TARD1B and REP_TARF1B schemas have been updated: Values from 0 to 9 are allowed for the “Sun_Corr”, “Moon_Corr”, “Gibbs_Corr”, “Sunglint_Corr”, “Backlobes_Corr” and “FTT_Corr” fields.
04-03-04	12-Mar-09	<ul style="list-style-type: none">→ Fixed bugs in REP_XAR__ and REP_XDAR__ schemas. The new versions are consistent with SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.8 2008-11-03 document.
04-03-05	02-Apr-09	<ul style="list-style-type: none">→ New DBL_SM_XXXX_AUX_PLM____0203.xsd delivered by DMS has been added to the schemas distribution.→ AUX_VTEC_C, AUX_VTEC_P and AUX_VTEC_R Header schemas have been updated. The “VTEC_PCD” format has been changed from %05d to %8s to be compliant with L1 Spec v5.7→ AUX_GAL2OS Data block schema has been updated to be consistent with L2 Spec v4.2→ MPL_JOBORD schema has been updated. “File_Counter” field has been removed from the “List_of_Brk_Files” structure to be consistent with SO-ID-IDR-GS-1001_1_10_ Generic IPF ICD (L2P-PR-0069 can be closed).→ REP_OSUDP2 and REP OSDAP2 Data block schemas have been modified: “Warning_Log_identifier” and “Error_Log_identifier” formats have been limited to 12 characters (delivered by ARGANS + “Warning_Log_identifier” format correction applied by Indra)→ REP_SMUDP2 and REP_SMDAP2 Data block schemas have been modified: “Warning_Log_identifier” and “Error_Log_identifier” formats have been limited to 12 characters (delivered by Array)
04-03-06	21-Apr-09	<ul style="list-style-type: none">→ AUX_VTEC_C, AUX_VTEC_P and AUX_VTEC_R Header schemas have been modified: “Ftp_Server” tag has been removed in order to be consistent with L1 Specs v5.8

Version	Date	Change Description
04-03-07	30-Apr-09	→ New DBL_SM_XXXX_AUX_CNFNRT_0202 schema delivered by DMS has been added to the distribution.
04-03-08	29-May-09	→ New schemas have been added to the distribution for the AUX_FARA_C, AUX_FARA_P and AUX_FARA_R, REP_FARA_C, REP_FARA_P, REP_FARA_R, AUX_CNFFAR, CNF_FARA__, according to L1 Spec v5.10
04-03-09	15-Jun-09	<p>→ DBL_SM_XXXX_AUX_CNFFAR has been modified:</p> <ul style="list-style-type: none"> ○ Tag names have been changed to consider them beginning with capital letter. ○ “Snapshot_Time_Sampling” format has been changed from %04d to %03d. ○ “Magnetic_Model_ID”, “Faraday_Computation_Method”, “VTEC_Model” and “Simulated_Orbit” formats have been changed from %02d to %01d. ○ “SW_Error_Counter” and “ADF_Error_Counter” formats have been changed from %08d to %05d. <p>→ HDR_SM_XXXX_AUX_FARA_C, HDR_SM_XXXX_AUX_FARA_P, HDR_SM_XXXX_AUX_FARA_R have been changed:</p> <ul style="list-style-type: none"> ○ Tag names have been changed to consider them beginning with capital letter. ○ “FARA_Algorithm_Configuration” structure has been added. It collects the Snapshot_Time_Sampling, Magnetic_Model_ID, Faraday_Computation_Method, VTEC_Model and Simulated_Orbit. ○ “Snapshot_Time_Sampling” format has been changed from %01d to %03d. <p>→ DBL_SM_XXXX_AUX_FARA_C, DBL_SM_XXXX_AUX_FARA_P, DBL_SM_XXXX_AUX_FARA_R have been modified:</p> <ul style="list-style-type: none"> ○ “Snapshot_Time” field has been removed. ○ “Faraday_Rotation_Angle_Specular_Ray”, “VTEC” and “VTEC_Specular_Ray” fields have been added.

Version	Date	Change Description
		<ul style="list-style-type: none"> → HDR_SM_XXXX_CNF_FARA__0200.xsd and DBL_SM_XXXX_CNF_FARA__0200.xsd have been renamed as HDR_SM_XXXX_CNF_FARADA_0200.xsd and DBL_SM_XXXX_CNF_FARADA_0200.xsd. Moreover, the “IGRF_File...” fields have been renamed as “Geomagnetic_File...” in accordance with SO-ID-IDR-GS-0008 v4.7 → DBL_SM_XXXX_MPL_ORBSCT schema has been modified. The “ANX_Longitude” format has been changed in order to allow values inside [-360,360] → New DBL_SM_XXXX_MPL_XBDPRE and HDR_SM_XXXX_MPL_XBDPRE schemas have been added to the distribution. → DBL_SM_XXXX_CNF_L2SM__0201.xsd delivered by Cecilia (Array) on April 27th has been added to the distribution: The “HW_Identifier” field has been introduced in the “List_of_Hosts” structure in order to have the same CNF_L2SM for all the PFWs. → DBL_SM_XXXX_MIR_SMDAP2_0201 and DBL_SM_XXXX_MIR_SMUDP2_0201 delivered by Cecilia (Array) on June 11th have been added to the distribution: The change is to resize all the counters that are dependent on the L1c BT_Data_Counter (which is 2 bytes) from 1 byte to 2 bytes.
04-03-10	24-Jun-09	<ul style="list-style-type: none"> → HDR_SM_XXXX_AUX_FARA_C, HDR_SM_XXXX_AUX_FARA_P and HDR_SM_XXXX_AUX_FARA_R schemas have been modified: The previous “string” types have been changed to “integer” to be consistent with L1 Specs v5.11 → New DBL_SM_XXXX_CNF_L2OS__0202 and DBL_SM_XXXX_CNF_L2OS__0203 schemas delivered by ARGANS (18/06/2009) have been added to the distribution → New DBL_SM_XXXX_REP_SCND1C_0203 and DBL_SM_XXXX_REP_SCNF1C_0203 delivered by DMS (23/06/2009) have been added to the distribution. → A bug in the schemas_config has been corrected in the “MPL_XBDPRE” structure. The starting and ending tags made reference to a wrong file.

Version	Date	Change Description
04-03-11	24-Jul-09	<ul style="list-style-type: none"> → HDR_SM_XXXX_MIR_CRSU1A_0202, HDR_SM_XXXX_MIR_CRSD1A_0202, DBL_SM_XXXX_MIR_CRSU1A_0202 and DBL_SM_XXXX_MIR_CRSD1A schemas delivered by DMS (22-07-09) have been added to the distribution. → DBL_SM_XXXX_AUX_PMS____0204 schema delivered by DMS has been added to the distribution. → DBL_SM_XXXX_AUX_DFFFRA_0201, DBL_SM_XXXX_AUX_DFFLMX_0201 and DBL_SM_XXXX_AUX_DFFXYZ_0201 provided by Array (21/07/09) have been added to the distribution: <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_DFFFRA: The fractions (e.g. FNO) and the class code (e.g. FNO_Class_Code) changed from character-8 to unsignedByte-8. ○ DBL_SM_XXXX_AUX_DFFLMX: LAI_Max changed from character-8 to unsignedByte-8. ○ DBL_SM_XXXX_AUX_DFFXYZ_0201: Zone_ID changed from long-64 to unsignedLong-64. → DBL_SM_XXXX_AUX_CNFL1P schema has been modified according to L1 Spec 5.12: <ul style="list-style-type: none"> ○ "RMS_Threshold_for_FTT" and "RMS_Threshold_for_NIR" formats have been changed from %06d to %03.2f ○ "Max_Mkj_RFI_Threshold" unit attribute has been removed.
04-03-12	20-Aug-09	<ul style="list-style-type: none"> → HDR_SM_XXXX_MIR_CRSU1A schema has been harmonized with HDR_SM_XXXX_MIR_CRSD1A, as it was indicated by DMS (A. Gutierrez e-mail, Aug 18th) → DBL_SM_XXXX_REP_SCND1C_0204 and DBL_SM_XXXX_REP_SCNF1C_0204 provided by DMS have been added to the distribution. → DBL_SM_XXXX_AUX_DGGXYZ_0201.binXschema.xml schema has been included in the proper folder (bug reported by Array on 4-Aug-09 has been corrected).
04-04-00	21-Sep-09	<ul style="list-style-type: none"> → DBL_SM_XXXX_AUX_AGDPT__0201, DBL_SM_XXXX_AUX_CNFOSD_0202, DBL_SM_XXXX_AUX_CNFOSF_0202, DBL_SM_XXXX_AUX_GAL2OS_0202 and DBL_SM_XXXX_CNFL2OS__0204 provided by ARGANS on Sept 11th have been added to the distribution. Please note that L2OS v309 will not work with previous versions of the schemas without these updates.



Version	Date	Change Description
		→ DBL_SM_XXXX_AUX_SPAR schema has been modified: "Temperature" unit attribute has been changed from °C to K, according to DMS and ESA recommendations (A.Gutierrez and R.Crapolicchio e-mails 07/09/09)
04-04-01	02-Oct-09	→ DBL_SM_XXXX_AUX_CNFL1P schema has been updated according to L1 Specs v5.13: <ul style="list-style-type: none">○ 0,1,2,3 values allowed for "Reflected_Sun_Correction_Type" field.○ "Max_Sunglint_Threshold" field has been added. → DBL_SM_XXXX_MIR_SC_D1B, DBL_SM_XXXX_MIR_SC_F1B, DBL_SM_XXXX_MIR_TARD1B, DBL_SM_XXXX_MIR_TARF1B have been updated: <ul style="list-style-type: none">○ 0,1,2,3 values allowed for "Reflected_Sun_Correction_Type" field. → HDR_SM_XXXX_MIR_SCND1B_0201, HDR_SM_XXXX_MIR_SCNF1B_0201 and DBL_SM_XXXX_AUX_CNFNRT_0203 delivered by DMS on October 1 st have been added to distribution (See Andrés Canales e-mail).
04-04-02	23-Oct-09	→ "DBL_SM_XXXX_REP_XIP__" schema has been updated: The allowed values for the "Status" field have been changed from "ERROR,WAITING" to COMPLETED", "ABORTED", "STARTED", "WAITING" in accordance with SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.9. This bug was detected during GSOV test EE-20.
04-04-03	04-Nov-09	→ DBL_SM_XXXX_REP_XAR__ and DBL_SM_XXXX_REP_XDAR__ schemas have been corrected: "String_Value" field has been added into the "Event" structure, in accordance with SO-ID-IDR-GS-0018 XBAS-CORE ICD v1.9
04-04-04	04-Dec-09	→ HDR_SM_XXXX_MPL_ORBSCT has been updated to not allow incoherent values in the "Validity_Start" and "Validity_Stop" fields (SCH-PR-0009 fixed).
04-04-05	23-Dec-09	→ DBL_SM_XXXX_AUX_RGHNS3_0202.xsd schema provided by ARGANS on December 2 nd has been added to the distribution.

Version	Date	Change Description
04-05-00	12-Feb-10	<ul style="list-style-type: none"> → “DBL_SM_XXXX_AUX_CNFL1P_0210.xsd” provided by DMS on February 9th has been included in the distribution: “Reference_Temperature_Level” field has been added. → “DBL_SM_XXXX_AUX_CNFNRT_0204.xsd” provided by DMS on February 9th has been included in the distribution: “Reference_Temperature_Level” field has been added.
04-06-00	04-Mar-10	<ul style="list-style-type: none"> → DBL_SM_XXXX_AUX_CNFSMD_0202, DBL_SM_XXXX_AUX_CNFSMF_0202 and HDR_SM_XXXX_MIR_SMUDP2_0201 provided by Array on Feb 25th have been added to the distribution: “Chi_2_Scale” field has been included.
04-07-00	24-Mar-10	<ul style="list-style-type: none"> → DBL_SM_XXXX_REP_PER_DA_, DBL_SM_XXXX_REP_PER_WE_, DBL_SM_XXXX_REP_PER_MO_, DBL_SM_XXXX_REP_PER_GL_ schemas have been modified in order to remove numeric restrictions in the “file_type” tag of the products. → DBL_SM_XXXX_AUX_CNFOSD_0203, DBL_SM_XXXX_AUX_CNFOSF_0203, AUX_OTT1D_, AUX_OTT1F_, AUX_OTT2D_, AUX_OTT2F_, AUX_OTT3D_, AUX_OTT3F_ provided by ARGANS on 10-March-2010 have been added to the distribution.
04-07-01	24-Mar-10	<ul style="list-style-type: none"> → Release to fix a bug in the schemas_config.xml related with the AUX_CNFOSD schema version.
04-07-02	03-May-10	<ul style="list-style-type: none"> → DBL_SM_XXXX_AUX_PMS___0205 provided by Deimos on 22nd April has been added to the distribution. → DBL_SM_XXXX_AUX_CNFL1P_0211 has been updated in order to increase the precision from 4 bytes to 5 bytes in the following fields: “Maximum_Separation_Within_Step”, “Maximum_Gap” and “Maximum_Sequence_Duration” → DBL_SM_XXXX_AUX_SPAR___0203 provided by Deimos on 29th April has been added to the distribution.
04-07-03	04-May-10	<ul style="list-style-type: none"> → DBL_SM_XXXX_AUX_CNFL1P schema has been updated in order to add “RFI_Filter_u-noise_threshold” field. → DBL_SM_XXXX_AUX_CNFNRT schema has been updated in order to add RFI_Filter_u-noise_threshold field and to increase the precision from 4 bytes to 5 bytes in the following fields: “Max_Separation_Within_Step”, “Max_Gap_Within_Sequence” and “Max_Sequence_Duration”



Version	Date	Change Description
		→ DBL_SM_XXXX_AUX_SPAR___ schema has been modified to organize the “Receiver_Path_Type” elements and attributes according to the convention defined in SO-MA-IDR-GS-0004 XML Guidelines v2.0 2007-11-16.
04-07-04	20-May-10	→ HDR_SM_XXXX_MIR_CRSD1A schema has been updated, as it was requested by R.Crapolicchio on May 14 th , allowing now 4 digits in the following fields: “Cons_Long_PMS_DSR_New”, “Cons_Long_PMS_DSR_Removed”, “Cons_Phase_FWF_Origin_New”, “Cons_Phase_FWF_Origin_Removed”, “Cons_Long_Ampl_FWF_Origin_New”, “Cons_Long_Ampl_FWF_Origin_Removed”.
04-07-05	20-Sep-10	→ DBL_SM_XXXX_MIR_ANIR1A_0201 and HDR_SM_XXXX_MIR_ANIR1A_0201 provided by DME on 17/09/2010 have been added to the distribution.
04-07-06	22-Oct-10	→ DBL_SM_XXXX_AUX_BULL_B_0200 and HDR_SM_XXXX_AUX_BULL_B_0200 have been added to the distribution in accordance with the specifications included in [AD.27.]
04-07-07	17-Nov-10	→ DBL_SM_XXXX_AUX_CNFNRT_0206.xsd provided by Deimos-Space (Esteban Santiago) on November 15 th has been added to the distribution. → DBL_SM_XXXX_AUX_BULL_B_0201.binXschema.xml has been modified after detecting some errors to ingest AUX_BULL_B products in SPQC.
04-07-08	23-Dec-10	→ DBL_SM_XXXX_AUX_RGHNS3_0203.binXschema.xml and HDR_SM_XXXX_AUX_RGHNS3_0201.xsd provided by ARGANS on Nov-22 have been added do the distribution. → DBL_SM_XXXX_AUX_CNFSMD_0203.xsd, DBL_SM_XXXX_AUX_CNFSMF_0203.xsd, DBL_SM_XXXX_AUX_ECMWF__0201.binXschema.xml DBL_SM_XXXX_MIR_SMUDP2_0202.binXschema.xml provided by Array on Nov-26 have been added to the distribution. → HDR_SM_XXXX_AUX_ECMCDF_0200.xsd and DBL_SM_XXXX_AUX_ECMCDF_0200.binXschema.xml delivered by Array (through S.Delwart) on Dec-20 have been added to the distribution. All these changes are reflected in L2 Specs v5.3.

Version	Date	Change Description
04-07-09	11-Feb-11	<p>→ The following schemas delivered by ESA (Jean-Claude.Debruyen) on Nov-08 have been added to the distribution:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_MIR_CRSD1A_0203.binXschema.xml ○ DBL_SM_XXXX_MIR_CRSU1A_0203.binXschema.xml ○ DBL_SM_XXXX_TLM_MIRA1A_0201.binXschema.xml <p>→ DBL_SM_XXXX_AUX_NIR____0204.xsd schema delivered by Deimos (Antonio Gutierrez) on Nov-09 has been added to the distribution.</p> <p>→ DBL_SM_XXXX_AUX_RFILST_0200.xsd and HDR_SM_XXXX_AUX_RFILST_0200.xsd delivered by R.Crapolicchio on Jan-26 have been added to the distribution.</p> <p>All these changes are in line with L1 Specs 5.19</p> <p>→ HDR_SM_XXXX_AUX_CNVNRT_0200.xsd, DBL_SM_XXXX_AUX_CNVNRT_0200.xsd, HDR_SM_XXXX_REP_BUFR____0200.xsd, DBL_SM_XXXX_REP_BUFR____0200.xsd, HDR_SM_XXXX_CNF_BUFRCV_0200.xsd, DBL_SM_XXXX_CNF_BUFRCV_0200.xsd delivered by Raffaele Crapolicchio on Feb 10th have been added to the distribution.</p> <p>The formats of these files are defined in SO-ID-DMS-GS-0004 – DPGS2BUFR ICD v2.0 and SO-ID-DMS-GS-0002 NRT Product Format v3.1.</p>
04-07-10	10-Jun-11	<p>→ DBL_SM_XXXX_REP_CLE_DA, DBL_SM_XXXX_REP_CLE_GL, DBL_SM_XXXX_REP_CLE_MO and DBL_SM_XXXX_REP_CLE_WE have been updated in order to allow 'S2D_____' and 'S2P_____' values for the "File_Type" tag, within "Aux_Tech_Data_List"/"record" structure (DPGS-PR-1765)</p> <p>→ HDR_SM_XXXX_AUX_VTEC_C, HDR_SM_XXXX_AUX_VTEC_P and HDR_SM_XXXX_AUX_VTEC_R have been updated to allow three digits instead of two for the "Solar_flux" values (VTEC-PR-0038)</p>
05-00-00	20-May-11	<p>→ The following schemas delivered by ARGANS (Paul Spurgeon) on May 9th have been added to the distribution:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_CNFOSD_0300.xsd ○ DBL_SM_XXXX_AUX_CNFOSF_0300.xsd ○ DBL_SM_XXXX_MIR_OSUDP2_0300.binXschema.xml <p>The format of these files is specified in SO-TN-IDR-GS-0006 v6.0</p> <p>→ The following schemas delivered by DEIMOS (Antonio Gutierrez) on May 10th have been added to the distribution:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_CNFL1P_0300.xsd

Version	Date	Change Description
		<ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_LCF___0300.xsd ○ DBL_SM_XXXX_MIR_CRSD1A_0300.binXschema.xml ○ HDR_SM_XXXX_MIR_CRSD1A_0300.xsd ○ DBL_SM_XXXX_MIR_CRSU1A_0300.binXschema.xml ○ HDR_SM_XXXX_MIR_CRSU1A_0300.xsd <p>The format of these files is specified in SO-TN-IDR-GS-0005 v5.20</p> <p>→ The following schemas delivered by Array (Ali Mahmoodi) on May 11th have been added to the distribution:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_CNFSMD_0300.xsd ○ DBL_SM_XXXX_AUX_CNFSMF_0300.xsd ○ DBL_SM_XXXX_MIR_SMUDP2_0300.binXschema.xml <p>The format of these files is specified in SO-TN-IDR-GS-0006 v6.0</p> <p>Remaining updates do not correspond to format changes but they are only increases in the major version of the schemas to indicate the new baseline (V500)</p>
05-00-01	10-Jun-2011	<p>→ DBL_SM_XXXX_AUX_ECMWF__0300.binXschema.xml, HDR_SM_XXXX_AUX_ECMWF__0300.xsd, DBL_SM_XXXX_AUX_DFFLAI_0300.binXschema.xml, HDR_SM_XXXX_AUX_DFFLAI_0300.xsd, DBL_SM_XXXX_AUX_VTEC_C_0300.binXschema.xml, HDR_SM_XXXX_AUX_VTEC_C_0300.xsd, DBL_SM_XXXX_AUX_VTEC_P_0300.binXschema.xml, HDR_SM_XXXX_AUX_VTEC_P_0300.xsd, DBL_SM_XXXX_AUX_VTEC_R_0300.binXschema.xml, HDR_SM_XXXX_AUX_VTEC_R_0300.xsd have been added to the distribution. They do not contain any format change regarding the previous version of the schemas but their versions have been increased to link them to V500 baseline.</p> <p>→ DBL_SM_XXXX_CNF_L1OP__ , DBL_SM_XXXX_CNF_PRL1BD, DBL_SM_XXXX_CNF_PRL1BF and DBL_SM_XXXX_CNF_FTTGEN have been updated: new tag “Logical_Processing_Centre_Name” has been added according to ESA (R. Crapolicchio) indications received by e-mail on May 20th</p>
05-00-02	04-Jul-2011	<p>→ DBL_SM_XXXX_CNF_SMPSP_0300 has been updated: “List_Of_Hosts” structure has been included to allow installing the same CNF_SMPSP_ file in all machines. The schema has been delivered by Array on July 4th</p> <p>Notice that DBL_SM_XXXX_MIR_CRSD1A_0204.binXschema.xml, DBL_SM_XXXX_MIR_CRSU1A_0204.binXschema.xml and DBL_SM_XXXX_TLM_MIRA1A_0202.binXschema.xml have been added to the distribution to align it with schemas_2011-06-28_v04-07-11.</p>



Version	Date	Change Description
05-00-03	16-Sep-2011	→ DBL_SM_XXXX_CNF_L2SM__0301 schema file delivered by Array on September 12 th has been added to the distribution. It has been updated to add “List_of_Multithreaded_Processors” and related types
05-01-00	25-Oct-2011	<p>→ HDR_SM_XXXX_AUX_VTEC_R has been updated in order to correct SCH-PR-0010: [CRITICAL] “Max_TEC value in VTEC_R cannot be on 4 digits”. The same update has been also applied to HDR_SM_XXXX_AUX_VTEC_C, HDR_SM_XXXX_AUX_VTEC_P to keep the VTEC product headers harmonized.</p> <p>→ HDR_SM_XXXX_AUX_CNFNRT_0300.xsd, DBL_SM_XXXX_AUX_CNFNRT_0300.xsd, HDR_SM_XXXX_AUX_N256__0300.xsd, DBL_SM_XXXX_AUX_N256__0300.binXschema.xml, L1C_NRT_Specific_Product_Report_0003.xsd, HDR_SM_XXXX_REP_SCND1C_0300.xsd, DBL_SM_XXXX_REP_SCND1C_0300.xsd, HDR_SM_XXXX_REP_SCNF1C_0300.xsd and DBL_SM_XXXX_REP_SCNF1C_0300.xsd delivered by Deimos on October 21st have been incorporated into the schemas distribution.</p> <p>→ DBL_SM_XXXX_AUX_OTT1D__0301.binXschema.xml, DBL_SM_XXXX_AUX_OTT1F__0301.binXschema.xml, DBL_SM_XXXX_AUX_OTT2D__0301.binXschema.xml, DBL_SM_XXXX_AUX_OTT2F__0301.binXschema.xml, DBL_SM_XXXX_AUX_OTT3D__0301.binXschema.xml, DBL_SM_XXXX_AUX_OTT3F__0301.binXschema.xml delivered by ARGANS (Paul Spurgeon) on October 24th have been added to the distribution.</p>
05-01-01	21-Nov-2011	→ DBL_SM_XXXX_AUX_CNFSMD_0301 and DBL_SM_XXXX_AUX_CNFSMF_0301 delivered by Array (Cecilia) on Nov-15 th have been incorporated into the distribution.
05-01-02	28-Nov-2011	→ -DBL_SM_XXXX_AUX_N256__0301.binXschema.xml delivered by Deimos-Space (Esteban Santiago Zambrano) on November 24 th has been incorporated into the distribution.
05-01-03	20-Jan-2012	→ “Logical_Processing_Centre_Name” tag has been added to DBL_SM_XXXX_CNF_BUFRCV_ schema to be compliant with CNF_BUFRCV file delivered with BUFRCV v1.5 SW
05-02-00	18-Apr-2012	→ DBL_SM_XXXX_MIR_CRSD1A_0301.binXschema.xml, DBL_SM_XXXX_MIR_CRSD1A_0301.binXschema.xml, HDR_SM_XXXX_MIR_CRSD1A_0301.xsd, HDR_SM_XXXX_MIR_CRSD1A_0301.xsd, DBL_SM_XXXX_MIR_CSTD1A_0300.binXschema.xml, DBL_SM_XXXX_MIR_CSTU1A_0300.binXschema.xml, HDR_SM_XXXX_MIR_CSTD1A_0300.xsd and

Version	Date	Change Description
		HDR_SM_XXXX_MIR_CSTU1A_0300.xsd delivered by DME (Antonio Gutierrez) on November 2 nd have been incorporated into the dpgs distribution. The schemas are consistent with SO-TN-IDR-GS-0005 L1 Spec v5.23 2012-02-24
05-02-01	15-May-2012	→ New version of schemas released to include REP_CSTD1A schema as it was requested by R. Crapolicchio by e-mail on 11-May-2012
06-00-00	14-Dec-2012	<p>New version of the schemas released to include the schemas delivered by Array and ARGANS for the L2 V600 baseline. The data formats are consistent with <u>L2 Spec v7.0 2012-12-14</u></p> <ul style="list-style-type: none"> • <u>For L2OS V600 baseline</u>, the following schemas are updated: <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_CNFOSD_0400.xsd ○ DBL_SM_XXXX_AUX_CNFOSF_0400.xsd ○ DBL_SM_XXXX_AUX_GAL2OS_0400.binXschema.xml ○ DBL_SM_XXXX_CNFL2OS_0400.xsd ○ DBL_SM_XXXX_MIR_OSDAP2_0400.binXschema.xml ○ DBL_SM_XXXX_MIR_OSUDP2_0400.binXschema.xml ○ DBL_SM_XXXX_AUX_DGGRFI_0400.binXschema.xml The HDR schemas associated to these DBL schemas have been copied with version 0400. • <u>For L2SM V600 baseline</u>: <p>The following schemas associated to new auxiliary data files are added:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_DFFSNO_0400.binXschema.xml ○ HDR_SM_XXXX_AUX_DFFSNO_0400.xsd ○ DBL_SM_XXXX_AUX_DFFSOI_0400.binXschema.xml ○ HDR_SM_XXXX_AUX_DFFSOI_0400.xsd <p>The following schemas are updated:</p> <ul style="list-style-type: none"> ○ DBL_SM_XXXX_AUX_CNFSMD_0400xsd ○ DBL_SM_XXXX_AUX_CNFSMF_0400.xsd ○ DBL_SM_XXXX_AUX_DGGFLO_0400.binXschema.xml ○ DBL_SM_XXXX_AUX_DGGROU_0400.binXschema.xml ○ DBL_SM_XXXX_AUX_DGGTFO_0400.binXschema.xml ○ DBL_SM_XXXX_AUX_DGGTLV_0400.binXschema.xml

Version	Date	Change Description
		<ul style="list-style-type: none"> ○ DBL_SM_XXXX_MIR_SMUDP2_0400.binXschema.xml ○ DBL_SM_XXXX_AUX_DGGRFI_0400.binXschema.xml <p>The HDR schemas associated to these DBL schemas have been copied with version 0400. <u>Please note that only new schemas have been versioned with major version "04" (those that have not suffered any change are kept with major version 03). The content of L2 and Post-processors Internal configuration files associated to V600 need to be updated in accordance with this approach.</u></p>
06-00-01	21-Dec-2012	New version issued to correct a bug in schemas_config.xml associated to schemas_2012-12-14_v06-00-00. The error was reported by Array (Cecilia) on December 18 th and it is related to a wrong value for the <PATH> parameter of AUX_DFFSOI file.
06-01-00	20-May-2013	<p>New schemas associated to L1OP v600 baseline are added to the distribution (sent by Deimos on 17/05/2013). The content of the following schemas is updated:</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFL1P_0400.xsd • DBL_SM_XXXX_AUX_PLM___0400.xsd • DBL_SM_XXXX_AUX_PMS___0400.xsd • HDR_SM_XXXX_MIR_BWLD1C_0400.xsd • HDR_SM_XXXX_MIR_BWLF1C_0400.xsd • HDR_SM_XXXX_MIR_BWSD1C_0400.xsd • HDR_SM_XXXX_MIR_BWSF1C_0400.xsd • DBL_SM_XXXX_MIR_CRSD1A_0400.binXschema.xml • HDR_SM_XXXX_MIR_CRSD1A_0400.xsd • DBL_SM_XXXX_MIR_CRSU1A_0400.binXschema.xml • DBL_SM_XXXX_MIR_FTTD___0400.binXschema.xml • HDR_SM_XXXX_MIR_FTTD___0400.xsd • DBL_SM_XXXX_MIR_FTTF___0400.binXschema.xml • HDR_SM_XXXX_MIR_FTTF___0400.xsd • DBL_SM_XXXX_MIR_SC_D1A_0400.binXschema.xml • HDR_SM_XXXX_MIR_SC_D1A_0400.xsd • HDR_SM_XXXX_MIR_SC_D1B_0400.xsd • DBL_SM_XXXX_MIR_SC_F1A_0400.binXschema.xml

Version	Date	Change Description
		<ul style="list-style-type: none"> • HDR_SM_XXXX_MIR_SC_F1A_0400.xsd • HDR_SM_XXXX_MIR_SC_F1B_0400.xsd • HDR_SM_XXXX_MIR_SCLD1C_0400.xsd • HDR_SM_XXXX_MIR_SCLF1C_0400.xsd • HDR_SM_XXXX_MIR_SCSD1C_0400.xsd • HDR_SM_XXXX_MIR_SCSF1C_0400.xsd • DBL_SM_XXXX_MIR_TARD1A_0400.binXschema.xml • HDR_SM_XXXX_MIR_TARD1A_0400.xsd • HDR_SM_XXXX_MIR_TARD1B_0400.xsd • DBL_SM_XXXX_MIR_TARF1A_0400.binXschema.xml • HDR_SM_XXXX_MIR_TARF1A_0400.xsd • HDR_SM_XXXX_MIR_TARF1B_0400.xsd <p>Regarding L2OS, the following schemas sent by Raffaele Crapolicchio on 25/04/2013, are included in the distribution:</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFOSD_0401.xsd • DBL_SM_XXXX_AUX_CNFOSF_0401.xsd
06-01-01	31-Jul-2013	<p>-DBL_SM_XXXX_AUX_CNFSMD_0401.xsd and DBL_SM_XXXX_AUX_CNFSMF_0401.xsd delivered by Array on July 23rd have been incorporated to the distribution.</p> <p>These new schemas are associated to L2SM v06.10 FAT</p> <p>-DBL_SM_XXXX_MIR_GMATD__0301.binXschema.xml and DBL_SM_XXXX_MIR_GMATU__0301.binXschema.xml schemas have been updated in order to allow 153664 columns instead of 65536</p> <p>-DBL_SM_XXXX_CNF_L0PGEN_0010.xsd has been updated to include "Cleaning_Parameters" structure</p>
06-02-00	04-Sep-2013	<p>The following schemas delivered by ARGANS for L2OS v610 and OSCOTT v610 have been incorporated into the distribution:</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFOSD_0402.xsd • DBL_SM_XXXX_AUX_CNFOSF_0402.xsd • DBL_SM_XXXX_AUX_DTBCUR_0400.binXschema.xml • HDR_SM_XXXX_AUX_DTBCUR_0400.xsd

Version	Date	Change Description
		<ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_DTBXY__0400.binXschema.xml • HDR_SM_XXXX_AUX_DTBXY__0400.xsd • DBL_SM_XXXX_CNF_L2OS__0401.xsd • DBL_SM_XXXX_CNF_OSCOTT_0400.xsd • HDR_SM_XXXX_CNF_OSCOTT_0400.xsd • DBL_SM_XXXX_REP_DTBCUR_0400.xsd • HDR_SM_XXXX_REP_DTBCUR_0400.xsd • DBL_SM_XXXX_REP_DTBXY__0400.xsd • HDR_SM_XXXX_REP_DTBXY__0400.xsd • DBL_SM_XXXX_REP_OTT1D__0400.xsd • HDR_SM_XXXX_REP_OTT1D__0400.xsd • DBL_SM_XXXX_REP_OTT1F__0400.xsd • HDR_SM_XXXX_REP_OTT1F__0400.xsd • DBL_SM_XXXX_REP_OTT2D__0400.xsd • HDR_SM_XXXX_REP_OTT2D__0400.xsd • DBL_SM_XXXX_REP_OTT2F__0400.xsd • HDR_SM_XXXX_REP_OTT2F__0400.xsd • DBL_SM_XXXX_REP_OTT3D__0400.xsd • HDR_SM_XXXX_REP_OTT3D__0400.xsd • DBL_SM_XXXX_REP_OTT3F__0400.xsd • HDR_SM_XXXX_REP_OTT3F__0400.xsd
06-02-01	13-Nov-2013	DBL_SM_XXXX_MIR_OSDAP2_0401.binXschema.xml delivered by IDEAS (Massimo Cardaci) on October 31 has been incorporated into the distribution.
06-02-02	07-Mar-2014	<ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFL1P_0401.xsd schema delivered by IPFTOOLS CCB (Raffaele Crapolicchio) on March 3rd has been added to the distribution. It includes the following changes regarding previous version: “BT_Dual_RFI_Tail_Flag_Threshold”, “BT_Full_RFI_Tail_Flag_Threshold”, “Estimated_RFI_Radius_Constant_A”, “Estimated_RFI_Radius_Constant_B”, “Maximum_RFI_Radius_Threshold” are added.

Version	Date	Change Description
		<ul style="list-style-type: none"> New version of Complex_Unit_Types file (Complex_Unit_Types_0018.xsd) is included to incorporate the complex type "K_float_04p3f_Unit_Type" required by new DBL_SM_XXXX_AUX_CNFL1P_0401.xsd
06-02-03	04-Apr-2014	<ul style="list-style-type: none"> DBL_SM_XXXX_AUX_CNFL1P_0402.xsd is updated to allow positive and negative values for "Estimated_RFI_Radius_Constant_A" and "Estimated_RFI_Radius_Constant_B", as is requested by IDEAS (R.Crapolicchio) on March 31.
06-03-00	22-Aug-2014	<ul style="list-style-type: none"> DBL_SM_XXXX_AUX_DTBCY__0402.binXschema.xml, DBL_SM_XXXX_AUX_CNFSMD_0402.xsd, DBL_SM_XXXX_AUX_CNFSMF_0402.xsd, DBL_SM_XXXX_AUX_DGGROU_0401.binXschema.xml, DBL_SM_XXXX_AUX_DGGTFO_0401.binXschema.xml, DBL_SM_XXXX_AUX_DGGTLV_0401.binXschema.xml delivered by Raffaele Crapolicchio on August 5th (e-mail : [SMOS] Schema change request) have been included in the dpgs schemas distribution <p>*Please note that DBL_SM_XXXX_AUX_DTBCY__0401.binXschema is a simply copy of DBL_SM_XXXX_AUX_DTBCY__0400.binXschema generated to keep consecutive versions in the dpgs delivery</p>
06-03-01	18-Mar-2015	DBL_SM_XXXX_AUX_CNFNRT_0301.xsd schema delivered by CEC on March 13 th has been added to the distribution
07-00-00	25-May-2015	<p>Version updated to incorporate the schemas delivered by DME on May 12th for L1OP V700 baseline. New schemas included in the distribution are: HDR_SM_XXXX_AUX_CNFL1P_0500.xsd</p> <ul style="list-style-type: none"> DBL_SM_XXXX_AUX_CNFL1P_0500.xsd HDR_SM_XXXX_AUX_LCF__0400.xsd DBL_SM_XXXX_AUX_LCF__0400.xsd HDR_SM_XXXX_MIR_CRSD1A_0500.xsd DBL_SM_XXXX_MIR_CRSD1A_0500.binXschema.xml HDR_SM_XXXX_MIR_SC_F1A_0500.xsd DBL_SM_XXXX_MIR_SC_F1A_0500.binXschema.xml HDR_SM_XXXX_MIR_UAVD1A_0400.xsd DBL_SM_XXXX_MIR_UAVD1A_0400.binXschema.xml HDR_SM_XXXX_TLM_MIRA1A_0400.xsd

Version	Date	Change Description
		<ul style="list-style-type: none"> • DBL_SM_XXXX_TLM_MIRA1A_0400.binXschema.xml
07-00-01	31-Aug-2015	<p>Version updated to include the schemas delivered by ESA (R. Crapolicchio) on August 7th for L1OP V700 baseline.</p> <p>New schemas added to the distribution are:</p> <ul style="list-style-type: none"> • HDR_SM_XXXX_AUX_LCF___0500.xsd • DBL_SM_XXXX_AUX_LCF___0500.xsd • HDR_SM_XXXX_AUX_PLM___0500.xsd • DBL_SM_XXXX_AUX_PLM___0500.xsd • HDR_SM_XXXX_MIR_SC_D1A_0500.xsd • DBL_SM_XXXX_MIR_SC_D1A_0500.binXschema.xml • HDR_SM_XXXX_MIR_TARD1A_0500.xsd • DBL_SM_XXXX_MIR_TARD1A_0500.binXschema.xml • HDR_SM_XXXX_MIR_TARF1A_0500.xsd • DBL_SM_XXXX_MIR_TARF1A_0500.binXschema.xml
07-01-00	18-Dec-2015	<p>New version released to include new schemas delivered by ARGANS on November 23rd to be used with L2OS v650. The following schemas have been updated regarding previous version:</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFOSD_0403.xsd • DBL_SM_XXXX_AUX_CNFOSF_0403.xsd • HDR_SM_XXXX_AUX_MSOTT__0400.xsd • DBL_SM_XXXX_AUX_MSOTT__0400.binXschema.xml • HDR_SM_XXXX_AUX_SGLINT_0400.xsd • DBL_SM_XXXX_AUX_SGLINT_0400.binXschema.xml • HDR_SM_XXXX_AUX_SUN_BT_0400.xsd • DBL_SM_XXXX_AUX_SUN_BT_0400.binXschema.xml

Version	Date	Change Description
07-02-00	01-Apr-2016	<p>Version released to include L2OS schemas to be used with L2OS V660 and new Snow Pre-processor schemas. The following L2OS schemas have been delivered by ESA (R.Crapolicchio on 17/03/2016):</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFOSD_0404.xsd • DBL_SM_XXXX_AUX_CNFOSF_0404.xsd • DBL_SM_XXXX_AUX_SSS__0400.binXschema.xml • HDR_SM_XXXX_AUX_SSS__0400.xsd • DBL_SM_XXXX_MIR_OSDAP2_0402.binXschema.xml • DBL_SM_XXXX_MIR_OSUDP2_0401.binXschema.xml <p>The following Snow Pre-processor schemas have been delivered by ESA (R.Crapolicchio on 21/03/2016) as well:</p> <ul style="list-style-type: none"> • HDR_SM_XXXX_CNF_SNOWP__0400.xsd • DBL_SM_XXXX_CNF_SNOWP__0400.xsd • DBL_SM_XXXX_REP_DFFSNO_0400.xsd • HDR_SM_XXXX_REP_DFFSNO_0400.xsd
07-02-01	18-Apr-2016	<p>DBL_SM_XXXX_REP_DFFSNO_0401 updated to change the type associated to Input_File from string_File_Name_60_Type to string_Type</p>
07-03-00	30-Aug-2016	<p>The following schemas delivered by L1 ESL (J. Barbosa) on 19/08/2016 have been included in the distribution:</p> <ul style="list-style-type: none"> • DBL_SM_XXXX_AUX_CNFL1P_0501.xsd • DBL_SM_XXXX_AUX_PMS__0401.xsd • DBL_SM_XXXX_MIR_SCLD1C_0401.binXschema.xml • DBL_SM_XXXX_MIR_SCLF1C_0401.binXschema.xml • DBL_SM_XXXX_MIR_SCSD1C_0401.binXschema.xml • DBL_SM_XXXX_MIR_SCSF1C_0401.binXschema.xml • HDR_SM_XXXX_MIR_SC_D1B_0401.xsd • HDR_SM_XXXX_MIR_SC_F1B_0401.xsd • HDR_SM_XXXX_MIR_TARD1B_0401.xsd • HDR_SM_XXXX_MIR_TARF1B_0401.xsd



Version	Date	Change Description
07-03-01	10-Jan-2017	DBL_SM_XXXX_AUX_CNFSMD_0403 and DBL_SM_XXXX_AUX_CNFSMF_0403 delivered by IDEAS (R.Crapolicchio) on 20/12/2016 have been incorporated to the distribution. Both schemas are applicable to L2 SM V650.
07-03-02	10-Jul-2017	DBL_SM_XXXX_AUX_CNFL1P_0502 updated to fix a wrong type associated to "Reconstruction_Image_Algorithm" parameter and to remove "Max_Snapshot_RFI_Threshold_04" in accordance with SO-TN-IDR-GS-0005_v6.3_L1 Spec (errors sent by R. Crapolicchio by e-mail under subject "[SMOS] Schema-v07-03-00 issues")
07-04-00	25-Oct-2017	The following schemas delivered by Deimos (Jose Barbosa) on October 17th have been incorporated to the distribution: 1) HDR_SM_XXXX_MIR_SC_F1B_0402.xsd, HDR_SM_XXXX_MIR_SC_D1B_0402.xsd, HDR_SM_XXXX_MIR_TARF1B_0402.xsd and HDR_SM_XXXX_MIR_TARD1B_0402.xsd: <ul style="list-style-type: none">• Addition of WGS84 data to SPH 2) DBL_SM_XXXX_MIR_SC_F1B_0401.binXschema, DBL_SM_XXXX_MIR_SC_D1B_0401.binXschema, DBL_SM_XXXX_MIR_TARF1B_0401.binXschema and DBL_SM_XXXX_MIR_TARD1B_0401.binXschema: <ul style="list-style-type: none">• Incorporation of Matrix_BFP2EF (9 double values) to DBL 3) DBL_SM_XXXX_AUX_NIR____0301.xsd: <ul style="list-style-type: none">• Added fixed Tna0 values• Added "L_P_H" and "R" parameters Tp7 latency correction 4) DBL_SM_XXXX_AUX_LCF____0501.xsd: <ul style="list-style-type: none">• Added G_kj correction factor for NIR and ALL-LICEF modes independently 5) DBL_SM_XXXX_AUX_PATT____0301.binXschema:

Version	Date	Change Description
		<ul style="list-style-type: none"> • Changed antenna patterns data. AP are now in the instrument frame grid of 196x196 <p>6) DBL_SM_XXXX_AUX_CNFL1P_0503.xsd:</p> <ul style="list-style-type: none"> • Corrected Reference_Temperature_Level and Earth_Contribution_Correction types in Prod. Spec. to Integer (were Flag type) • Added fixed TP7 correction flag (Tp7_Correction_Flag) • Added fixed Tna flag (Fixed_Tna_Flag) • Added Sun Removal option for new improved Sun estimation <p>7) Added new DBL_SM_XXXX_AUX_FRSNEL_0001.binXschema and HDR_SM_XXXX_AUX_FRSNEL_0001 for Static Fresnel Model</p>
07-04-01	10-Nov-2017	<p>Basic_Types_0017.xsd slightly modified to allow the value 4 for the type "integer_01d_Direct_Correction_Type_Type" associated to "Direct_Sun_Correction_Type".</p> <p>The change is requested by Deimos (Gonçalo Lopes) on 02/11/2017 (e-mail under subject [SMOS]: Small L1OP v720 schema issue)</p>
07-04-02	20-Dec-2017	<p>New schemas DBL_SM_XXXX_RQC_RAWDES_0001.xsd, HDR_SM_XXXX_RQC_RAWDES_0001.xsd, DBL_SM_XXXX_RQD_RAWDES_0001.xsd, HDR_SM_XXXX_RQD_RAWDES_0001.xsd have been added to the distribution, as it was agreed with Raffaele Caproliccio by e-mail (13/12/2017, subject: RE: SPQC telecon notes). See SPQC-PR-0273 for details.</p>
07-04-03	31-Jan-2018	<p>New version of schemas delivered to fix SCH-PR-0012 "[ROUTINE] Typo in schema_config.xml" is fixed</p>

Table 6-1: Change List

7. XML SCHEMAS

The list attached below details all the schemas contained within the delivery:

SM_XXXX_AUX_AGDPT__0201
SM_XXXX_AUX_AGDPT__0200
SM_XXXX_AUX_AGDPT__0100
SM_XXXX_AUX_AGDPT__0007
SM_XXXX_AUX_AGDPT__0006
SM_XXXX_AUX_AGDPT__0005
SM_XXXX_AUX_AGDPT__0004
SM_XXXX_AUX_AGDPT__0003
SM_XXXX_AUX_AGDPT__0002
SM_XXXX_AUX_AGDPT__0001

SM_XXXX_AUX_APDL00_0010
SM_XXXX_AUX_APDL00_0009
SM_XXXX_AUX_APDL00_0008
SM_XXXX_AUX_APDL00_0007
SM_XXXX_AUX_APDL00_0006
SM_XXXX_AUX_APDL00_0005
SM_XXXX_AUX_APDL00_0004
SM_XXXX_AUX_APDL00_0003
SM_XXXX_AUX_APDL00_0002
SM_XXXX_AUX_APDL00_0001

SM_XXXX_AUX_APDL__0300
SM_XXXX_AUX_APDL__0200
SM_XXXX_AUX_APDL__0101
SM_XXXX_AUX_APDL__0100

SM_XXXX_AUX_APDNRT_0200
SM_XXXX_AUX_APDNRT_0100

SM_XXXX_AUX_APDS00_0011
SM_XXXX_AUX_APDS00_0010
SM_XXXX_AUX_APDS00_0009
SM_XXXX_AUX_APDS00_0008

SM_XXXX_AUX_APDS00_0007
SM_XXXX_AUX_APDS00_0006
SM_XXXX_AUX_APDS00_0005
SM_XXXX_AUX_APDS00_0004
SM_XXXX_AUX_APDS00_0003
SM_XXXX_AUX_APDS00_0002
SM_XXXX_AUX_APDS00_0001

SM_XXXX_AUX_APDS__0300
SM_XXXX_AUX_APDS__0201
SM_XXXX_AUX_APDS__0200
SM_XXXX_AUX_APDS__0103
SM_XXXX_AUX_APDS__0102
SM_XXXX_AUX_APDS__0101
SM_XXXX_AUX_APDS__0100

SM_XXXX_AUX_ATMOS__0300
SM_XXXX_AUX_ATMOS__0200
SM_XXXX_AUX_ATMOS__0100
SM_XXXX_AUX_ATMOS__0008
SM_XXXX_AUX_ATMOS__0007
SM_XXXX_AUX_ATMOS__0006
SM_XXXX_AUX_ATMOS__0005
SM_XXXX_AUX_ATMOS__0004
SM_XXXX_AUX_ATMOS__0003
SM_XXXX_AUX_ATMOS__0002
SM_XXXX_AUX_ATMOS__0001

SM_XXXX_AUX_AUXCT__0001

SM_XXXX_AUX_BFP___0300
SM_XXXX_AUX_BFP___0201
SM_XXXX_AUX_BFP___0200
SM_XXXX_AUX_BFP___0101
SM_XXXX_AUX_BFP___0100
SM_XXXX_AUX_BFP___0010
SM_XXXX_AUX_BFP___0009

SM_XXXX_AUX_BFP___0008
SM_XXXX_AUX_BFP___0007
SM_XXXX_AUX_BFP___0006
SM_XXXX_AUX_BFP___0005
SM_XXXX_AUX_BFP___0004
SM_XXXX_AUX_BFP___0003
SM_XXXX_AUX_BFP___0002
SM_XXXX_AUX_BFP___0001

SM_XXXX_AUX_BIGWBF_0008
SM_XXXX_AUX_BIGWBF_0007
SM_XXXX_AUX_BIGWBF_0006
SM_XXXX_AUX_BIGWBF_0005
SM_XXXX_AUX_BIGWBF_0004
SM_XXXX_AUX_BIGWBF_0003
SM_XXXX_AUX_BIGWBF_0002
SM_XXXX_AUX_BIGWBF_0001

SM_XXXX_AUX_BNDLST_0200
SM_XXXX_AUX_BSCAT__0300
SM_XXXX_AUX_BSCAT__0201
SM_XXXX_AUX_BSCAT__0200
SM_XXXX_AUX_BSCAT__0100

SM_XXXX_AUX_BULL_B_0300
SM_XXXX_AUX_BULL_B_0201
SM_XXXX_AUX_BULL_B_0200

SM_XXXX_AUX_BWGHT__0300
SM_XXXX_AUX_BWGHT__0200
SM_XXXX_AUX_BWGHT__0101
SM_XXXX_AUX_BWGHT__0100

SM_XXXX_AUX_CNFFAR_0201
SM_XXXX_AUX_CNFFAR_0200

SM_XXXX_AUX_CNFL0P_0100

SM_XXXX_AUX_CNFL0P_0008
SM_XXXX_AUX_CNFL0P_0007
SM_XXXX_AUX_CNFL0P_0006
SM_XXXX_AUX_CNFL0P_0005
SM_XXXX_AUX_CNFL0P_0004
SM_XXXX_AUX_CNFL0P_0003
SM_XXXX_AUX_CNFL0P_0002
SM_XXXX_AUX_CNFL0P_0001

SM_XXXX_AUX_CNFL1P_0503
SM_XXXX_AUX_CNFL1P_0502
SM_XXXX_AUX_CNFL1P_0501
SM_XXXX_AUX_CNFL1P_0500
SM_XXXX_AUX_CNFL1P_0402
SM_XXXX_AUX_CNFL1P_0401
SM_XXXX_AUX_CNFL1P_0400
SM_XXXX_AUX_CNFL1P_0300
SM_XXXX_AUX_CNFL1P_0212
SM_XXXX_AUX_CNFL1P_0211
SM_XXXX_AUX_CNFL1P_0210
SM_XXXX_AUX_CNFL1P_0209
SM_XXXX_AUX_CNFL1P_0208
SM_XXXX_AUX_CNFL1P_0207
SM_XXXX_AUX_CNFL1P_0206
SM_XXXX_AUX_CNFL1P_0205
SM_XXXX_AUX_CNFL1P_0204
SM_XXXX_AUX_CNFL1P_0203
SM_XXXX_AUX_CNFL1P_0202
SM_XXXX_AUX_CNFL1P_0201
SM_XXXX_AUX_CNFL1P_0200
SM_XXXX_AUX_CNFL1P_0101
SM_XXXX_AUX_CNFL1P_0100
SM_XXXX_AUX_CNFL1P_0010
SM_XXXX_AUX_CNFL1P_0009
SM_XXXX_AUX_CNFL1P_0008
SM_XXXX_AUX_CNFL1P_0007
SM_XXXX_AUX_CNFL1P_0006

SM_XXXX_AUX_CNFL1P_0005
SM_XXXX_AUX_CNFL1P_0004
SM_XXXX_AUX_CNFL1P_0003
SM_XXXX_AUX_CNFL1P_0002
SM_XXXX_AUX_CNFL1P_0001

SM_XXXX_AUX_CNFNRT_0301
SM_XXXX_AUX_CNFNRT_0300
SM_XXXX_AUX_CNFNRT_0206
SM_XXXX_AUX_CNFNRT_0205
SM_XXXX_AUX_CNFNRT_0204
SM_XXXX_AUX_CNFNRT_0203
SM_XXXX_AUX_CNFNRT_0202
SM_XXXX_AUX_CNFNRT_0201
SM_XXXX_AUX_CNFNRT_0200
SM_XXXX_AUX_CNFNRT_0101
SM_XXXX_AUX_CNFNRT_0100

SM_XXXX_AUX_CNFOSD_0404
SM_XXXX_AUX_CNFOSD_0403
SM_XXXX_AUX_CNFOSD_0402
SM_XXXX_AUX_CNFOSD_0401
SM_XXXX_AUX_CNFOSD_0400
SM_XXXX_AUX_CNFOSD_0300
SM_XXXX_AUX_CNFOSD_0203
SM_XXXX_AUX_CNFOSD_0202
SM_XXXX_AUX_CNFOSD_0201
SM_XXXX_AUX_CNFOSD_0200

SM_XXXX_AUX_CNFOSF_0404
SM_XXXX_AUX_CNFOSF_0403
SM_XXXX_AUX_CNFOSF_0402
SM_XXXX_AUX_CNFOSF_0401
SM_XXXX_AUX_CNFOSF_0400
SM_XXXX_AUX_CNFOSF_0300
SM_XXXX_AUX_CNFOSF_0203
SM_XXXX_AUX_CNFOSF_0202



SM_XXXX_AUX_CNFOFSF_0201
SM_XXXX_AUX_CNFOFSF_0200

SM_XXXX_AUX_CNFOFS2_0100
SM_XXXX_AUX_CNFOFS2_0008
SM_XXXX_AUX_CNFOFS2_0007
SM_XXXX_AUX_CNFOFS2_0006
SM_XXXX_AUX_CNFOFS2_0005
SM_XXXX_AUX_CNFOFS2_0004
SM_XXXX_AUX_CNFOFS2_0003
SM_XXXX_AUX_CNFOFS2_0002
SM_XXXX_AUX_CNFOFS2_0001

SM_XXXX_AUX_CNFOPOS_0001

SM_XXXX_AUX_CNFSMD_0402
SM_XXXX_AUX_CNFSMD_0401
SM_XXXX_AUX_CNFSMD_0400
SM_XXXX_AUX_CNFSMD_0301
SM_XXXX_AUX_CNFSMD_0300
SM_XXXX_AUX_CNFSMD_0203
SM_XXXX_AUX_CNFSMD_0202
SM_XXXX_AUX_CNFSMD_0201
SM_XXXX_AUX_CNFSMD_0200

SM_XXXX_AUX_CNFSMF_0402
SM_XXXX_AUX_CNFSMF_0401
SM_XXXX_AUX_CNFSMF_0400
SM_XXXX_AUX_CNFSMF_0301
SM_XXXX_AUX_CNFSMF_0300
SM_XXXX_AUX_CNFSMF_0203
SM_XXXX_AUX_CNFSMF_0202
SM_XXXX_AUX_CNFSMF_0201
SM_XXXX_AUX_CNFSMF_0200

SM_XXXX_AUX_CNFSM2_0100
SM_XXXX_AUX_CNFSM2_0006

SM_XXXX_AUX_CNFSM2_0005
SM_XXXX_AUX_CNFSM2_0004
SM_XXXX_AUX_CNFSM2_0003
SM_XXXX_AUX_CNFSM2_0002
SM_XXXX_AUX_CNFSM2_0001
SM_XXXX_AUX_CNVNRT_0200

SM_XXXX_AUX_COMNCT_0001

SM_XXXX_AUX_CONF____0001

SM_XXXX_AUX_DFFFRA_0300
SM_XXXX_AUX_DFFFRA_0201
SM_XXXX_AUX_DFFFRA_0200
SM_XXXX_AUX_DFFFRA_0100
SM_XXXX_AUX_DFFFRA_0009
SM_XXXX_AUX_DFFFRA_0008
SM_XXXX_AUX_DFFFRA_0007
SM_XXXX_AUX_DFFFRA_0006
SM_XXXX_AUX_DFFFRA_0005
SM_XXXX_AUX_DFFFRA_0004
SM_XXXX_AUX_DFFFRA_0003
SM_XXXX_AUX_DFFFRA_0002
SM_XXXX_AUX_DFFFRA_0001

SM_XXXX_AUX_DFFLAI_0300
SM_XXXX_AUX_DFFLAI_0200
SM_XXXX_AUX_DFFLAI_0100
SM_XXXX_AUX_DFFLAI_0010
SM_XXXX_AUX_DFFLAI_0009
SM_XXXX_AUX_DFFLAI_0008
SM_XXXX_AUX_DFFLAI_0007
SM_XXXX_AUX_DFFLAI_0006
SM_XXXX_AUX_DFFLAI_0005
SM_XXXX_AUX_DFFLAI_0004
SM_XXXX_AUX_DFFLAI_0003
SM_XXXX_AUX_DFFLAI_0002

SM_XXXX_AUX_DFFLAI_0001

SM_XXXX_AUX_DFFLMX_0300

SM_XXXX_AUX_DFFLMX_0201

SM_XXXX_AUX_DFFLMX_0200

SM_XXXX_AUX_DFFLMX_0100

SM_XXXX_AUX_DFFLMX_0009

SM_XXXX_AUX_DFFLMX_0008

SM_XXXX_AUX_DFFLMX_0007

SM_XXXX_AUX_DFFLMX_0006

SM_XXXX_AUX_DFFLMX_0005

SM_XXXX_AUX_DFFLMX_0004

SM_XXXX_AUX_DFFLMX_0003

SM_XXXX_AUX_DFFLMX_0002

SM_XXXX_AUX_DFFLMX_0001

SM_XXXX_AUX_DFFSOI_0400

SM_XXXX_AUX_DFFSNO_0400

SM_XXXX_AUX_DFFXYZ_0300

SM_XXXX_AUX_DFFXYZ_0200

SM_XXXX_AUX_DFFXYZ_0100

SM_XXXX_AUX_DFFXYZ_0009

SM_XXXX_AUX_DFFXYZ_0008

SM_XXXX_AUX_DFFXYZ_0007

SM_XXXX_AUX_DFFXYZ_0006

SM_XXXX_AUX_DFFXYZ_0005

SM_XXXX_AUX_DFFXYZ_0004

SM_XXXX_AUX_DFFXYZ_0003

SM_XXXX_AUX_DFFXYZ_0002

SM_XXXX_AUX_DFFXYZ_0001

SM_XXXX_AUX_DGG____0300

SM_XXXX_AUX_DGG____0200

SM_XXXX_AUX_DGG____0100

SM_XXXX_AUX_DGG____0009

SM_XXXX_AUX_DGG____0008
SM_XXXX_AUX_DGG____0007
SM_XXXX_AUX_DGG____0006
SM_XXXX_AUX_DGG____0005
SM_XXXX_AUX_DGG____0004
SM_XXXX_AUX_DGG____0003
SM_XXXX_AUX_DGG____0002
SM_XXXX_AUX_DGG____0001

SM_XXXX_AUX_DGGFLO_0400
SM_XXXX_AUX_DGGFLO_0300
SM_XXXX_AUX_DGGFLO_0200
SM_XXXX_AUX_DGGFLO_0100
SM_XXXX_AUX_DGGFLO_0007
SM_XXXX_AUX_DGGFLO_0006
SM_XXXX_AUX_DGGFLO_0005
SM_XXXX_AUX_DGGFLO_0004
SM_XXXX_AUX_DGGFLO_0003
SM_XXXX_AUX_DGGFLO_0002
SM_XXXX_AUX_DGGFLO_0001

SM_XXXX_AUX_DGGFRA_0001

SM_XXXX_AUX_DGGGEO_0001

SM_XXXX_AUX_DGGRFI_0400
SM_XXXX_AUX_DGGRFI_0300
SM_XXXX_AUX_DGGRFI_0200
SM_XXXX_AUX_DGGRFI_0100
SM_XXXX_AUX_DGGRFI_0008
SM_XXXX_AUX_DGGRFI_0007
SM_XXXX_AUX_DGGRFI_0006
SM_XXXX_AUX_DGGRFI_0005
SM_XXXX_AUX_DGGRFI_0004
SM_XXXX_AUX_DGGRFI_0003
SM_XXXX_AUX_DGGRFI_0002
SM_XXXX_AUX_DGGRFI_0001

SM_XXXX_AUX_DGGROU_0401
SM_XXXX_AUX_DGGROU_0400
SM_XXXX_AUX_DGGROU_0300
SM_XXXX_AUX_DGGROU_0200
SM_XXXX_AUX_DGGROU_0100
SM_XXXX_AUX_DGGROU_0008
SM_XXXX_AUX_DGGROU_0007
SM_XXXX_AUX_DGGROU_0006
SM_XXXX_AUX_DGGROU_0005
SM_XXXX_AUX_DGGROU_0004
SM_XXXX_AUX_DGGROU_0003
SM_XXXX_AUX_DGGROU_0002
SM_XXXX_AUX_DGGROU_0001

SM_XXXX_AUX_DGGTFO_0401
SM_XXXX_AUX_DGGTFO_0400
SM_XXXX_AUX_DGGTFO_0300
SM_XXXX_AUX_DGGTFO_0200
SM_XXXX_AUX_DGGTFO_0100
SM_XXXX_AUX_DGGTFO_0008
SM_XXXX_AUX_DGGTFO_0007
SM_XXXX_AUX_DGGTFO_0006
SM_XXXX_AUX_DGGTFO_0005
SM_XXXX_AUX_DGGTFO_0004
SM_XXXX_AUX_DGGTFO_0003
SM_XXXX_AUX_DGGTFO_0002
SM_XXXX_AUX_DGGTFO_0001

SM_XXXX_AUX_DGGTLV_0401
SM_XXXX_AUX_DGGTLV_0400
SM_XXXX_AUX_DGGTLV_0300
SM_XXXX_AUX_DGGTLV_0200
SM_XXXX_AUX_DGGTLV_0100
SM_XXXX_AUX_DGGTLV_0008
SM_XXXX_AUX_DGGTLV_0007
SM_XXXX_AUX_DGGTLV_0006

SM_XXXX_AUX_DGGTLV_0005
SM_XXXX_AUX_DGGTLV_0004
SM_XXXX_AUX_DGGTLV_0003
SM_XXXX_AUX_DGGTLV_0002
SM_XXXX_AUX_DGGTLV_0001

SM_XXXX_AUX_DGGVER_0006
SM_XXXX_AUX_DGGVER_0005
SM_XXXX_AUX_DGGVER_0004
SM_XXXX_AUX_DGGVER_0003
SM_XXXX_AUX_DGGVER_0002
SM_XXXX_AUX_DGGVER_0001

SM_XXXX_AUX_DGGXYZ_0300
SM_XXXX_AUX_DGGXYZ_0201
SM_XXXX_AUX_DGGXYZ_0200
SM_XXXX_AUX_DGGXYZ_0100
SM_XXXX_AUX_DGGXYZ_0008
SM_XXXX_AUX_DGGXYZ_0007
SM_XXXX_AUX_DGGXYZ_0006
SM_XXXX_AUX_DGGXYZ_0005
SM_XXXX_AUX_DGGXYZ_0004
SM_XXXX_AUX_DGGXYZ_0003
SM_XXXX_AUX_DGGXYZ_0002
SM_XXXX_AUX_DGGXYZ_0001

SM_XXXX_AUX_DISTAN_0300
SM_XXXX_AUX_DISTAN_0200
SM_XXXX_AUX_DISTAN_0100
SM_XXXX_AUX_DISTAN_0008
SM_XXXX_AUX_DISTAN_0007
SM_XXXX_AUX_DISTAN_0006
SM_XXXX_AUX_DISTAN_0005
SM_XXXX_AUX_DISTAN_0004
SM_XXXX_AUX_DISTAN_0003
SM_XXXX_AUX_DISTAN_0002
SM_XXXX_AUX_DISTAN_0001

SM_XXXX_AUX_DTBCUR_0400

SM_XXXX_AUX_DTBXY__0402

SM_XXXX_AUX_DTBXY__0401

SM_XXXX_AUX_DTBXY__0400

SM_XXXX_AUX_ECMCDF_0200

SM_XXXX_AUX_ECMWF__0300

SM_XXXX_AUX_ECMWF__0201

SM_XXXX_AUX_ECMWF__0200

SM_XXXX_AUX_ECMWF__0100

SM_XXXX_AUX_ECMWF__0009

SM_XXXX_AUX_ECMWF__0008

SM_XXXX_AUX_ECMWF__0007

SM_XXXX_AUX_ECMWF__0006

SM_XXXX_AUX_ECMWF__0005

SM_XXXX_AUX_ECMWF__0004

SM_XXXX_AUX_ECMWF__0003

SM_XXXX_AUX_ECMWF__0002

SM_XXXX_AUX_ECMWF__0001

SM_XXXX_AUX_ECOLAI_0200

SM_XXXX_AUX_FAIL__0300

SM_XXXX_AUX_FAIL__0200

SM_XXXX_AUX_FAIL__0101

SM_XXXX_AUX_FAIL__0100

SM_XXXX_AUX_FAIL__0011

SM_XXXX_AUX_FAIL__0010

SM_XXXX_AUX_FAIL__0009

SM_XXXX_AUX_FAIL__0008

SM_XXXX_AUX_FAIL__0007

SM_XXXX_AUX_FAIL__0006

SM_XXXX_AUX_FAIL__0005

SM_XXXX_AUX_FAIL__0004

SM_XXXX_AUX_FAIL___0003
SM_XXXX_AUX_FAIL___0002
SM_XXXX_AUX_FAIL___0001
SM_XXXX_AUX_FARA_C_0202
SM_XXXX_AUX_FARA_C_0201
SM_XXXX_AUX_FARA_C_0200

SM_XXXX_AUX_FARA_P_0202
SM_XXXX_AUX_FARA_P_0201
SM_XXXX_AUX_FARA_P_0200

SM_XXXX_AUX_FARA_R_0202
SM_XXXX_AUX_FARA_R_0201
SM_XXXX_AUX_FARA_R_0200

SM_XXXX_AUX_FLTSEA_0300
SM_XXXX_AUX_FLTSEA_0201
SM_XXXX_AUX_FLTSEA_0200
SM_XXXX_AUX_FLTSEA_0100
SM_XXXX_AUX_FLTSEA_0100
SM_XXXX_AUX_FLTSEA_0008
SM_XXXX_AUX_FLTSEA_0007
SM_XXXX_AUX_FLTSEA_0006
SM_XXXX_AUX_FLTSEA_0005
SM_XXXX_AUX_FLTSEA_0004
SM_XXXX_AUX_FLTSEA_0003
SM_XXXX_AUX_FLTSEA_0002
SM_XXXX_AUX_FLTSEA_0001

SM_XXXX_AUX_FOAM___0300
SM_XXXX_AUX_FOAM___0200
SM_XXXX_AUX_FOAM___0100
SM_XXXX_AUX_FOAM___0008
SM_XXXX_AUX_FOAM___0007
SM_XXXX_AUX_FOAM___0006
SM_XXXX_AUX_FOAM___0005
SM_XXXX_AUX_FOAM___0004



SM_XXXX_AUX_FOAM__0003

SM_XXXX_AUX_FOAM__0002

SM_XXXX_AUX_FOAM__0001

SM_XXXX_AUX_FRSNEL_0001

SM_XXXX_AUX_GAL2OS_0400

SM_XXXX_AUX_GAL2OS_0300

SM_XXXX_AUX_GAL2OS_0202

SM_XXXX_AUX_GAL2OS_0201

SM_XXXX_AUX_GAL2OS_0200

SM_XXXX_AUX_GAL2OS_0100

SM_XXXX_AUX_GAL_OS_0300

SM_XXXX_AUX_GAL_OS_0200

SM_XXXX_AUX_GAL_OS_0100

SM_XXXX_AUX_GAL_OS_0007

SM_XXXX_AUX_GAL_OS_0006

SM_XXXX_AUX_GAL_OS_0005

SM_XXXX_AUX_GAL_OS_0004

SM_XXXX_AUX_GAL_OS_0003

SM_XXXX_AUX_GAL_OS_0002

SM_XXXX_AUX_GAL_OS_0001

SM_XXXX_AUX_GAL_SM_0300

SM_XXXX_AUX_GAL_SM_0200

SM_XXXX_AUX_GAL_SM_0100

SM_XXXX_AUX_GAL_SM_0007

SM_XXXX_AUX_GAL_SM_0006

SM_XXXX_AUX_GAL_SM_0005

SM_XXXX_AUX_GAL_SM_0004

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SM_XXXX_AUX_GANIR__0001

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SM_XXXX_AUX_GLXY__0002
SM_XXXX_AUX_GLXY__0001

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SM_XXXX_AUX_GRIDW__0001

SM_XXXX_AUX_ITCOEF_0001

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SM_XXXX_AUX_LOPGEN_0001

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SM_XXXX_AUX_VTEC_C_0100

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SM_XXXX_AUX_VTEC_C_0008

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SM_XXXX_AUX_VTEC_C_0005

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SM_XXXX_AUX_VTEC_C_0001

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SM_XXXX_AUX_VTEC_R_0200

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SM_XXXX_CMD_HLPLAN_0002
SM_XXXX_CMD_HLPLAN_0001

SM_XXXX_CMD_XBDOWN_0003



SM_XXXX_CMD_XBDOWN_0002

SM_XXXX_CMD_XBDOWN_0001

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SM_XXXX_CNF_BUFRCV_0200

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SM_XXXX_CNF_ECOLAI_0200

SM_XXXX_CNF_FARADA_0200

SM_XXXX_CNF_FARA__0200

SM_XXXX_CNF_L0P1__0100

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SM_XXXX_CNF_FTTGEN_0200

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SM_XXXX_CNF_JMATR__0200

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SM_XXXX_CNF_PRL1BF_0300
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SM_XXXX_CNF_VTECGN_0200

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SM_XXXX_CNF_RPL0P2_0004
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SM_XXXX_CNF_RPL0P2_0002
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SM_XXXX_MPL_PROTEV_0001

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SM_XXXX_MPL_RULESF_0001

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SM_XXXX_TLM_MIRA0__0004
SM_XXXX_TLM_MIRA0__0003
SM_XXXX_TLM_MIRA0__0002
SM_XXXX_TLM_MIRA0__0001

SM_XXXX_TLM_MIRA1A_0400
SM_XXXX_TLM_MIRA1A_0300
SM_XXXX_TLM_MIRA1A_0202
SM_XXXX_TLM_MIRA1A_0201
SM_XXXX_TLM_MIRA1A_0200
SM_XXXX_TLM_MIRA1A_0100
SM_XXXX_TLM_MIRA1A_0011
SM_XXXX_TLM_MIRA1A_0010



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SM_XXXX_TLM_MIRA1A_0009
SM_XXXX_TLM_MIRA1A_0008
SM_XXXX_TLM_MIRA1A_0007
SM_XXXX_TLM_MIRA1A_0006
SM_XXXX_TLM_MIRA1A_0005
SM_XXXX_TLM_MIRA1A_0004
SM_XXXX_TLM_MIRA1A_0003
SM_XXXX_TLM_MIRA1A_0002
SM_XXXX_TLM_MIRA1A_0001